

Name And Designation: Battu Veera Reddy , Assistant Professor

Email ID: veerareddy57@gmail.com

Educational qualification

- M.Tech in VLSI stream from Gokaraju Rangaraju Institute of Engineering & Technology, Hyderabad in the year 2013 with **76.84%**.
- B.Tech in Electronics and Communication Engineering from TRR Engineering College, Hyderabad in the year 2010 with **69.23%**.
- Intermediate (M.P.C) from S.V junior College, Board of intermediate education with an aggregate of **77%**, completed in 2006, podili.
- SSC from Govt boy's high school, Board of secondary education with an aggregate of **74%** completed in 2004, Podili.

Courses Taught:

- Programming, Data Structures And Algorithms Using Python
- JAVA Lab
- VLSI Design Lab
- IOMP LAB
- AE LAB

Professional Background:

- Assistant Professor (Department of ECE), GRIET, Hyderabad – Jul 2014 to till date

Administrative Experience:

- Exam Branch valuation Coordinator
- III Coordinator
- III Mentor

Publications:

- “Pseudo Random Number Generation Using LFSR and Low Power LFSR” in ICPCES-2014 at [Anna University](#).
- “weighted pseudo random based deterministic bist for fault testing and self healing”(ICCES2019)
- “FPGA Implementation of SIC Pair Generator” 5th International Conference on Communication and Electronics Systems (ICCES 2020)