About me:

Mrs.G.L.SumaLata, Assistant Professor of Electronics and Communication Engineering, pursuing Ph.D. in KL University, Hyderabad and has over ten plus years of academic experience in India. Her Ph.D. work is on Memristors for VLSI Design . Prior to Ph.D. she had earned B.Tech in Electronics and Communication Engineering , and M.Tech in Embedded Systems. Her research interest include VLSI Design, Embedded Systems and DeepLearning.

She has taught various courses like Digital Electronics,Microprocessors and Microcontrollers,VLSI Design theory and laboratories.She successfully guided U.G. as well as P.G. student projects.She encourages her students to participate in Hackathons by mentoring them and also to publish Research articles etc.

She has been serving the department in roles like Class coordinator, mentor,Lab External Examination Incharge along with Dr.Swaraja,Professor,Dept of ECE,looking after smooth conduction of External Lab Exams,Alumni Coordinator along with Mr.M.Kiran,Associate Professor,maintaining contacts with Alumni students,Overall Class Coordinator for III year(2020-21)etc.

She is an enthusiastic OBE Practitioner and supports department by involving actively in filing NBA ,NAAC,IQAC related documents etc. She is a member of Criteria1 NAAC Committee

She actively participates in various FDPs updating her skills.She is a guest faculty for Police Training classes dealing "Basic Electronics" subject for both PCs and SIs,getting Appreciation letters from Telangana Police Academy for the same.

Contact:

G.L.Suma Lata

Assistant Professor

Gokaraju Rangaraju Institute of Engineering and Technology

Hyderabad, Telangana, India (500090)

Email:sumaprasadgl@gmail.com

CURRICULUM VITAE:

Educational Details:

- Pursuing PhD (Electronics and Communication Engineering) in ULSI (Memristors based VLSI Circuits) specialization under the guidance of Dr.Abhishek Kumar Shrivatsava ,Associate Professor, KL University Hyderabad
- MTech [Embedded Systems] , GRIET, HYD.(Affiliated to JNTUH),Hyderabad
- BTech [ECE] , Gudlavalleru Engineering College (Affiliated to JNTU), Gudlavalleru

Professional Background:
- Assistant Professor, Electronics and Communication Engineering, GRIET,Hyderabad, 2012-Present
- Assistant Professor, Electronics and Communication Engineering, CMRCET,Hyderabad, 2011-2012
- Assistant Professor, Electronics and Communication Engineering, HRD Engineering college ,Hyderabad, , 2004-2007
- Associate Lecturer, Electronics and Communication Engineering, SVL polytechnic college,Machilipatnan, 2003-2004

Administrative Experience:

Involved in Certificates Verification at the time of Admission of First Year students of B.Tech

Committee Work:

Member (department NAAC criteria1) , GRIET, Since Dec 2018 to till date.

Member(IQAC) for dept of ECE

Member (department NBA committee), GRIET, Since Dec 2013 till date

Department Alumini Coordinator along with Mr.M.Kiran,Associate Professor,GRIET

Overall Class Coordinator for III year for Academic Year 2020-2021

Class Coordinator for III year C sec (2020-2021) I sem

Mentor(18241A04M5 to 18241A04O0 and 19245A0419 to 19245A0424)

Member in Coordinating Lab External Exams along with Dr.Swaraja,Professor GRIET

Member in Committee(Scientific Forestep)

Courses Taught:

UG:

Theory

Digital Electronics

Microprocessors and Micro Controllers

VLSI Design

Labs

Digital Electronics Lab

Microprocessors and MicroControllers Lab

VLSI Design Lab

PG

Embedded Systems Lab

PUBLICATIONS:

1. Sashank , G L SumaLata , Ravi Tej , Abhiram , Pavan Kalyan, Anurag, Arvind "Classification of brain tumor using 25 layer model of convolution neural networks" in IJAST(International Journal of Advanced Science and Technology),Vol.29 No. 29 No.2(2020)(Scopus Indexed)

2. N. Nikhitha, G. L. Sumalata, Ch. Pratyusha Chowdari"Performance Evaluation of Stochastic Gammatone Filters" in ICDSML(International conference on Data Science, machine learning and applications(2019),Springer Link pages 31-41(Scopus indexed).

3. A paper presented on "Impact of urbanization on Air pollution in Hyderabad-India " in Proceedings , at Telangana Environment Congress 2018 organised by Centre for Environment and Development on 22nd and 23rd March 2018,Hyderabad, 2018- 03-22

4.Swetha Nagaveni ,G .L. SumaLata (2016), Wireless Voice Controlled Fire Extinguished Robot ,International Journal of Scientific Engineering and Technology and Research, 9945-9948,volume 5, Issue 8 (ISSN 2319-8885), Impact factor-3.59)

5.P.Rajani,G.L.SumaLata, (aug 2015), Optimization of 64 bit Multiplier using Carry save adder and its DSP Application using Cadence, International Journal of Advanced Research in Computer and Communication Engineering.vol 4, issue 8,ISSN online(2278-1021) , impact factor-5.947

6.vishali, G.L.SumaLata (Feb 2015), Fast Fourier Transformation of vibration signals using Qt creator,IJETMR,page 85 to 89,vol2,issue 2 ,(ISSN 2348-4845), Impact factor-3.5540)

7.Hema Santhoshi,G L SumaLata(JAN 2012), To Design and Implement Data Acquisition System Based on ARM and CAN Protocol , National Conference on Advances in Communication Technologies (NCACT 2012) at GITAM University, Visakhapatnam.

PROFESSIONAL WORK:

Talks:
1.Delivered guest lecture as per the timetable in the duration of 5.2.18 to 28.2.18 on subject "Basic Electronics " for Technical Training for Police constables ,in police Communications organization, Telangana Hyderabad
 2. Delivered guest lecture as per the timetable in months of may and june 2018 on subject "Basic Electronics " for Technical Training for Police SI, in Police Communications organization, Telangana

Seminars and Symposiums:

FDPs organized:
- An FDP is organized on VLSI Digital Signal Processing from 28th to 30th January 2016 at GRIET.

FDPs Participated:

Name of the Event	Organiser	Date
Two week ISTE workshop on Analog Electronics at GRIET from 4.6.2013	GRIET	4.6.2013
A workshop on Estimation Theory on Digital Signal Processing and Cognitive Radio networks from 15.10.15 to 17.10.15	GRIET,Hyd	15.10.15 to 17.10.15
Two day Hands on FDP on Software Defined Radio at GRIET,HYD from 16.12.2015	GRIET,Hyd	16.12.2015
Current Research Trends in computer science GRIET ,HYD	GRIET,Hyd	2015-12-18 to 2015-12-19
Two day Hands on FDP on Software Defined Radio GRIET ,HYD	GRIET,Hyd	2015-12-16 2015-12-17
Outcome Based Education And Accreditation JNTUH,Hyd	JNTUH,Hyd	2015-09-14
A workshop attended on Case study based workshop on IOT and machine learning GRIET HYD	GRIET	2018-02- 01 2018-02- 03
National seminar on Biosignal processing for health care applications	KL,HYDERABAD	2019-12-17 to 2019-12-19
Applications of Deep learning for signal and image processing using python GRIET	GRIET,Hyd	2019-12-10 to 2019-12-12
One week online FDP on "Future	06.07.2020 MGIT	

NanoElectronics and Devices"		
DIGITAL TRANSFORMATION	NITTT/SWAYAM	6th to 22nd April 2020
webinar on Advanced digital system design using verilog	SVC Engineering college with LUCID	2020 Jun-29
fdp research topics in VLSI and industry trends 3 day by GMR INSTITUTE	GMR Institute	2020 May-29
webinar on synthesizable subset of verilog	GRIET	2020 May-25
(APSSDC FDP) 25.05.2020 to 06.06.2020	APSSDC	2020 May-25
1 WEEK FDP ON LATEX,R PROGRAMMING,PYTHON BY GNU IITB	GNU IITB	2020 Jun-24
it's Applications" organized	G PullaReddy Engineering College	2020 May-23
attended leadership talkLeadership talk With Mr Mahesh Babu CEO Mahindra Electric Mobility Ltd.	LEADERSHIP TALK	2020 Jun-22
one week Advanced Data Science and its Applications	BVRIT women	2020 Jun-22
one week FDP on AWS cloud computing	APSSC	2020 May-22
National Level Quiz on " Test Your Skills of Internet of Things (IoT) by MIT	MIT	2020 May-22
National Level Quiz on "Test Your Skill For NET /SET Paper_I Teaching & Research Aptitude by MIT	MIT	2020 May-22
Education 4.0 -- The Future of Learning in the Pandemic World	PSR engg college	2020 May-22
quiz on Technological and Digital Transformation for a New Era-Certificate by GNITS(women)	GNITS	2020 May-20
Cadence Webinar Functional Verification using Cadence Incisive	eduncle	2020 May-20

tool		
attended online FDP-ECE-ALIET 5 days	ALIET	2020 May-18
VLSI DFT Webinar	VSD	2020 May-18
Webinar on "VLSI Based System Design Using FPGA/SoC	apply volt	2020 May-16
Leadership Talk with Dr Pramod Choudary Founder & Chairman @PrajIndustries	IIC,MHRD	2020 Jun-15
one week FDP on Applied Machine Learning and deep learning	APPSC with RedPine Signals	2020 Jun-15
one week online Faculty Development Program (FDP) on "Deep Learning &	LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING	2020 May-15
Essence of Bhagavad Gita for Educators" on 15 May 2020	FDP	2020 May-14
faculty Awarness Program on NAAC ACCREDITATION	shri chhatrapati shivajiraje college of engineering	2020 May-14
faculty program on NBA	Bharati vidyapeeth college of engineering	2020 May-13
webinar on next generation AI technologies for cyber physical systems	SPEC	2020 May-13
MARGHADARSHAN	AICTE FDP	2020 May-12
webinar on Research Workflows.Research Metrics and Excellence in Academics Institutes	AICTE and Elseiver	2020 Jun-08
FDP on recent trends in computer Architecture,VLSI and Embedded systems	GRIET	2020 Jun-08
Two week FDP on "Easy way of writing the survey Paper"	NSRIT	2020 Jun-07
TOOLS FOR MAKING YOUR	usha Rama college of engineering and	2020 Jun-07

THESIS COMPLETE	technology	
webinar on Analog VLSI Design FLOW	GRIET	2020 Jun-06
WEBINAR On AI and ML Approaches in Speech Systems	GRIET	2020 May-06
cloud_enabled VLSI SoC design 5 day workshop using open_ source EDA tools	ELECTRONICS & ICT ACADEMIES NIT PATNA	2020 Jun-02
One-Week Online Faculty Development Program on Recent Trends in VLSI	GRIET	2020 May-30
Webinar on positive approach of mind during COVID 19 by ENVIS-HUB &EPTRI,TSPCB	EPTRI	2020 May-30

External Examiner :

- Lab External Examiner for VLSI Design Lab and MPMC Lab
- Moderator for MPMC and VLSI Design courses at GRIET
- Paper setter and valuator for different courses in other colleges

AWARDS AND ACHIEVEMENTS:

1.Certification in "Digital Transformation in Teaching and Learning" course of two weeks under TEQIP III by NITT/SWAYAM April 6th to 22nd 2020

2.Certification for two week Course completion from APSSDC on Embedded systems from 25.05.2020 to 06.06.2020

3.Elite Cerification from NPTEL "Microprocessors and Microcontrollers " online Course on 2018-05-05

4. Foundation program in ICT for education from 2017-08- 03 to 2017-09- 13

 5. Pedagogy for online and blended teaching - learning process IITBOMBAY online course 2017-09-14 to 2017-11- 20

Appreciations

1.Appreciation Letter for Police Training for Technical Training for Police constables ,in police Communications organization, Telangana Hyderabad from 5.2.18 to 28.2.18
2. .Appreciation Letter for Police Training for Technical Training for Police SIs , in may and june 2018 in police Communications organization, Telangana Hyderabad