

Name And Designation: G V SUBBA REDDY



About Myself:

I (G V Subba Reddy) associate professor in the Electronics and Communication department of Gokaraju Rangaraju institute of Engineering and Technology(Autonomous),JNTUH,Hyderabad.My research interests mainly on VLSI design,Signal processing(Signal,Image,Video).

Email ID:gvsreddy2005@gmail.com

Educational Details:

1. Ph.D. (pursuing) in Sathyabama University –chennai
Specialization: speech processing
2. M.TECH –Bharath University Chennai
Specialization: VLSI design,year:2005-2007
3. B.TECH -jntu Hyderabad
Specialization: Electronics and Communication Engineering
Year:1998-2002

Experience: 15 years

Professional Background:

Teaching:

1. Associate Professor in ECE Department Of GRIET (Autonomous), JNTUH ,Hyderabad since 2012 to till date
2. Assistant Professor in ECE Department Of GRIET ,JNTUH, Hyderabad since 2007 to 2012
3. Assistant Professor in ECE Department Of Dr.SGIT ,JNTUH, Markapur since 2002 to 2005

Courses Taught:

A. Under Graduate Level(B.TECH)

1. Electronic Devices and Circuits
2. Electronic Circuits and Analysis
3. Linear IC Applications
4. VLSI Design
5. Digital signal processing
6. Electronic Measurements and Instrumentation
7. Computer Networks
8. Radar Engineering
9. Pulse and Digital circuits
10. Satellite communications
11. Control Engineering

B. Post Graduate Level(M.TECH)

1. Analog IC Design
2. Analog and Digital IC Design
3. Vlsi design
4. Low Power VLSI Design
5. Advanced Digital Signal Processing

PUBLICATIONS:

1. Design and Implementation of 128X128 bit multiplier by Ancient Mathematics 2014
2. Power Optimised implementation of 32 bit carry look ahead adder using 16nm carbon nano tube technology 2015
3. Detection of Soft Errors in 64-Bit Register Files using Self Immunity Technique,□- Anjaneyulu Borelli, G.V.Subba Reddy. IJATIR,ISSN 2348â€“2370 Vol.07,Issue.15, October-2015, Pages:2896-2901
4. A Study of Sample Matrix Inversion Algorithm for Smart Antenna Applications 2016
5. An Analysis of Sample Matrix Algorithm for Smart Antenna Applications, The IUP Journal of Telecommunications 2016
6. Estimation of Path Loss in The Received Signal Under Lognormal Shadowing in 4G Systems 2016□
7. ZigBee Transmitter For IOT Wireless Devices ,VLSICS -UGC JOURNAL2017
8. High speed multiplier using Nikhila Sutra algorithm of Vedic mathematics2019
9. AUTOMATIC NUMBER PLATE DETECTION SYSTEM USING RASBERRY PI 2019

Workshops attended/organized

From Date	Till Date	Workshop/FDP name	Institution, Place
2019-12-05	2019-12-07	DEEP LEARNING	GRIET, HYDERABAD
2019-11-11	2019-11-22	MACHINE LEARNING IN SPEECH PROCESSING	KITS, WARANGAL, TS
2019-03-11	2019-03-15	STTp, Measurements and control for industrial automation	NITTTP, GRIET, Hyderabad
2019-01-07	2019-01-09	my DAQ and my RIO with LABVIEW Platform	GRIET, HYDERABAD
2018-05-07	2018-05-08	Computer Simulation Technology Studio (Organised and participated)	GRIET, Hyderabad
2017-10-05	2017-11-11	LABVIEW workshop 2 organised (for 2nd & 3rd Birthday.tech)	GRIET, Hyderabad
2017-09-18	2017-09-20	LABVIEW workshop organised (for 2nd & 3rd Birthday.tech)	GRIET, Hyderabad
2017-08-02	2017-12-31	Seminars for m.tech-vlsi (2017-18), semester -1 (internal)	GRIET, Hyderabad
2016-08-08	2016-09-04	Teaching with MOODLE (Aug 2016)	MOODLE HQ
2016-07-25	2016-07-30	FDP: Achieving Academic Excellence	IIM, Raipur
2016-07-18	2016-07-22	Phase -II: LabVIEW and NI USRP	GRIET, Hyderabad
2016-06-27	2016-07-01	Phase -I: LabVIEW and NI USRP	GRIET, Hyderabad
2016-06-27	2016-07-22	Organizing committee Phase - I & II: LabVIEW and NI USRP	GRIET, Hyderabad
2016-01-07	2016-03-07	Educational Technology for Engineering Teachers	IIT, Bombay
2015-12-16	2015-12-17	Software Defined Radio	GRIET, Hyderabad
2015-10-08	2015-12-05	Technical Communication	IIT, Bombay
2015-07-29	2015-07-04	FDP: Guide To a Passionate Teacher	GRIET, Hyderabad
2013-06-04	2013-06-14	Analog Electronics	IIT, Kharagpur
2010-12-17	2010-12-18	Organizing committee IC-MID 2010	GRIET, NovateII
2008-11-28	2008-11-30	The Cadence Digital IC Design Training	Central university of Hyderabad

25 th MAY 2020	30 th MAY 2020	Spoken Tutorial (FDP)	IIT, Bombay
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22 nd MAY 2020	26 th MAY2020	Artificial Intelligence(FDP)	BSI PVT.. Ltd
1 st june 2020	5 th june 2020	Role of Artificial Intelligence in Electronics	Sathyabhama university
11 th may 2020	15 th may2020	Artificial Intelligence(FDP)	ATAL Academy
22 nd April2020	2 nd May2020	LATEX	SG University, kolhapur
4 th may2020	6 th MAY2020	ML&AI	ALIET,Vijayavada
6 th may2020		Spoken Tutorial	ICT,MHRD

Memberships:

Member of IETE

AWARDS AND ACHIEVEMENTS:

100% Pass certificate in LOW POWER VLSI DESIGN ,M.Tech VLSI DESIGN 2009

100% Pass certificate in Analog IC Design ,M.Tech VLSI DESIGN 2010

Certifications&Appreciations:

A.NPTEL

1. Foundations of Wavelets and Multirate Digital Signal Processing MAR-APRIL2016

2. Electromagnetic theory JANUARY-APRIL2016

3. Estimation for Wireless Communications MIMO (or) OFDM Cellular and Sensor Networks JANUARY-APRIL2016

4. Basic Electronics JANUARY-APRIL2018

B.COURSERA:

1. Create Employee Management System with When I work

2. Control of Mobile Robots

3. AI For Everyone

4. Machine Learning Algorithms: Supervised learning tip and tail

5. Getting Started with AWS Machine Learning

6. Machine Learning for Business Professionals

C.UDHEMY:

1. Artificial Intelligence

2. Machine learning , data science ,deep learning with python