

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Introduction

Department of Electronics and Communication Engineering (ECE) was established in the year 1997. It conducts a B.Tech program in ECE and two M.Tech programs in Embedded Systems and VLSI. The Department has several well-equipped laboratories, namely, Microwave Engineering, Digital Communications, Electronic Computer Aided Design and VLSI, Digital Signal Processing, Microprocessors and Microcomputers, IC Applications, Analog Communications, Electrical Engineering, Pulse and Digital Circuits, Electronic Circuit Analysis, Electronic Devices and Circuits, Simulation, Computer Programming, Engineering Drawing, Engineering Physics and Chemistry, and Advanced English Language Communication Skills.

The Department has been accredited by NBA first from 2006 to 2009 which was then renewed from 2009 to 2012.

The Department motivates students to take up innovative projects through participation in an active in-house product development Lab. Industry interaction is being promoted through several projects.

Doctorates in ECE

S.No	Name of the Faculty	Degree Awarded	No. of Publications (Journals/Conferences)
1	Dr. T. Jagannadha Swamy	2016	10
2	Dr. T. Padma	2014	12
3	Dr. V Ayyem Pillai	2014	04
4	Dr.G. Mamatha	2013	10
5	Dr.Ch. Usha Kumari	2015	17
6	Dr V. Aravind	2016	2
7	Dr. D. Jayanthi	2013	16
8	Dr. K. Padmavathi	2016	28
9	Dr. K. Swaraja	2016	7
10	Dr. V. Sree Hari	2015	61
11	Dr. N. Arun Vignesh	2016	6

Research Projects/FDP's/Seminar Grants/Sanctioned

S.No	Name of the Faculty	Title	Name of the Funding Agency	Amount in Lakhs	Year
1.	Dr. T.C.Sarma	MODROBS	AICTE	18	2013

Resource Mobilization for Research (TEQIP-II)

S.No	Name of the Faculty	Title	Name of the Funding Agency	Amount	Year
1.	A.Radhanand	Embedded system to through Arduino	TEQIP-II	17,875	2013

Faculty and their publications:

Dr. T Jagannadha Swamy, Professor & Head of ECE

Qualifications: Ph.D (JNTUH, 2016), B.Tech (ECE) (Nagarjuna University, 2001), M.E. (ECE) (Osmania University, 2006)

Experience: 14 years

Research Interest: Digital Signal Processing, Wireless Embedded Networks



Journal Publications/ Conference Proceedings/ Chapters in Textbooks: 10

Chapters in Textbooks:

1. March 2016: Mr. T.Jagannadha Swamy, Associate professor contributed as an author for a book chapter with the following details:
Swamy, T. J., & Murthy, G. R. (2016). Cost Minimization of Sensor Placement and Routing in Wireless Sensor Networks: Placement and Routing Issues in a Random Plane. In N. Chilamkurti (Ed.), Emerging Innovations in Wireless Networks and Broadband Technologies (pp. 115-134). Hershey, PA: Information Science Reference. doi:10.4018/978-1-4666-9941-0.ch006.

Journal Publications:

International Publications:

1. T. Jagannadha Swamy, "Novel Energy Efficient Dynamic Routing Protocol for Wireless Sensor Networks, (IJARCS), 5(7), 2014, pp 9-15.
2. "A Novel Power Reduction Design using Approximate Adders for Inexact Computing" in International Journal of Science, Engineering and Technology Research, Volume...3.Issue...10., October-2014, pp.2565.-.2569
3. Energy Efficient Architecture to Cognitive Radio Wireless Sensor Networks International Journal of Computer Networks and Wireless Communications (IJCNCW), ISSN: 2250-3501, 2013
4. Non Uniform Grid based Cost minimization in Wireless Sensor Networks International Journal on Wireless Networks and Broadband Technologies (IJWNBT) DOI: 10.4018/IJWNBT, ISSN: 2155-6261, EISSN: 2155-627X, March 2012

Conference Proceedings:

International Proceedings:

1. "Link Evaluation of Uniform Grid Based Wireless Sensor Networks to Base Station with Leveling and Clustering ICCCI, 2013 DOI: 10.1109/ICCCI.2013.6466146.
2. Spectrum sensing: Approximations for Eigen value ratio based detection ICCCI 2012 DOI: 10.1109/ICCCI.2012.6158914, January, 2012
3. Maximizing the overall throughput of Radio Frequency Spectrum Management in Cognitive Radio Environment ICACM 2011 ISBN:978-93-81269-40-4

National Proceedings

1. DEEP: Dynamic Energy Efficient Protocol to Wireless Sensor Networks for Secure Routing ICMID 2010 .
2. Adaptive Digital Beam forming for Radar antennas National Conference on CCAES, 2011

Dr.T Padma, Professor, NSS Coordinator and Head of BME

Qualifications: PhD(ECE) From JNTUH, M.E(Medical Electronics) From Anna University in 1997, B-Tech (ECE) From RECW in 1993.

Experience: 20 years

Research Interest: *ECG Signal Processing*

Journal Publications/ Conference Proceedings: 12

Journal publications:

International Journals:

1. Tatiparti Padma, Chepuri Bhavana, Peddamallu Srividya, "Wavelet Based ECG Signal Component Identification", International Journal of Research in Engineering and Technology, Volume 03, Issue 05, May 2014, Pg 703 – 706, eISSN: 2319-1163|pISSN: 2321-7308.
2. Padma T, Madhavi Latha M, Abrar Ahmed, "ECG Compression and Lab View Implementation", Journal of Biomedical Sciences and Engineering Scientific Research USA, 2009, 2, 177-183
3. Padma T, Madhavi Latha M, "Compression Techniques for ECG Transmission", Medical Journal of DY Patil University Kolhapur, 2009, 2, 85-91

Conference Proceedings:

International Proceedings:

1. T. Padma, P. Manju Bhargavi, S. Bhargavi, K. Satish Kumar, "Image data compression using DWT and Bit plane slicing for Space applications", International Conference on Recent Advances in Design, Development and Operation of Micro Air Vehicles (IC-RAMAV-2013), JNTU, Hyderabad, ISBN: 9789351071693, Pg. 167-169, Elsevier Publications
2. T.Padma, Satish Kumar Kotha, "Application of ECG Compression Technique on Ventricular Tachycardia Signals" in (Exploit current research for harnessing the field of Life sciences) the Biofest-2012, international conference, Hyd , conference proceedings pg 18.
3. T.Padma, Satish Kumar Kotha, "Application of DCT Technique for compression of Supra Ventricular Arrhythmia Signals" for 2nd International Conference on Biomedical Engineering and Assistive Technologies (BEATS 2012), National Institute of Technology, Jalandhar, INDIA.
4. Padma T, Madhavi Latha M, Jaya Kumar K, Balaji B, "Designing a Low Power Sigma Delta Modulator and HRV Analysis for ECG System", International Conference on Advanced Computing Techniques, GRIET, Hyderabad, 2008, 939-944
5. Padma T, Madhavi Latha M, Jaya Kumar K, "Decision Making Algorithm Through LVQ Neural Network for ECG Arrhythmias", International Conference of Biomedical Engineering , International Federation for Medical and Biological Engineering Springer Link Singapore, 2008, 85-91
6. Padma T, Madhavi Latha M, Jaya Kumar K, "Compression Techniques for ECG Transmission", International Conference of Biomedical Engineering & Nanotechnology, DY Patil University Kolhapur, 2008
7. Padma T, Madhavi Latha M, "PC Based Wireless ECG Monitoring System", Spring 2006 Faculty Student Multidiscipline International Conference on Research and Teaching, OOI Consortium for Teaching, Research, Learning and Development USA, 2006, 25-29



National Proceedings:

1. T. Padma, Satish Kumar Kotha, "Discrete Cosine Transform for ECG Compression", 44th Mid-Term Symposium on ICT in Space, Defense and Industrial Applications, IETE Hyderabad, 2013.
2. T.Padma, Satish Kumar Kotha, " DCT compression on Malignant Ventricular Arrhythmia signals" PIC-2012, IETE Pallakad, conference proceedings pg 150-154

Dr. V Ayyem Pillai, Professor.

Qualifications: AMIE(Electronics & Communication Engineering) 1998, Institution of Engineers, India, ME(Industrial Electronics), Maharaja Sayajirao University of Baroda, Ph.D (2014) (Anna University)

Experience: 12 Years (Teaching) 15 years (Industry-Indian Air Force)

Research Interest: *Antennas, Smart antennas, MIMO, Signal Processing*

Journal Publications/ Conference Proceedings: 9



Journal Publications:

International Publications

1. G. V. Subba Reddy, V. Ayyem Pillai, A Study of Sample Matrix Inversion Algorithm for Smart Antenna Applications, Volume 9, Issue 15, April 2016 –IJST
2. Ayyem Pillai, V & Sudhakar, R 2013, 'A Low Complexity Near-Optimal MIMO Antenna Subset Selection Algorithm for Capacity Maximisation,' Hindawi, International Journal of Antennas and Propagation, vol. 2013, Article ID 956756, 11 pages, 2013. doi:10.1155/2013/956756 (ISSN 1687-5869, Thomson Reuters JCR impact factor 0.827)
3. Ayyem Pillai, V & Sudhakar, R 'Computationally efficient norm-based capacity-Maximizing MIMO antenna selection algorithms', Springer, National Academy Science Letters- Published online DOI: 10.1007/s40009-014-0255-2 (ISSN 0250-541X, Thomson Reuters JCR impact factor 0.240), 2014.
4. Ayyem Pillai, V, "Decoupled Transmit/Receive Incremental Successive MIMO Antenna Selection For Capacity Maximisation" Journal of Theoretical and Applied Information Technology (E-ISSN 1817-3195 / ISSN 1992-8645) accepted for publication. (Scopus Index)

Conference Proceedings

International Proceedings:

1. Ayyem Pillai, V & Sudhakar, R 2013, 'V-BLAST detectors: Performance investigation in correlated condition and time analysis', Proceedings - 2013 IEEE International Multi Conference on Automation, Computing, Control, Communication and Compressed Sensing, iMac4s, pp. 98-103, DOI: <http://dx.doi.org/10.1109/iMac4s.2013.6526390>

National Proceedings:

1. G Haritha, Dr V Ayyem Pillai, Adaptive Beamforming algorithms for smart antennas, Jan 22, 23 2016, National conference on recent advancement in Electronics, 2016 at IFHE, Hyderabad.
2. K Mounika, Dr V Ayyem Pillai, Analyses of LMS Algorithm for Smart Antenna Applications Jan 22, 23 2016, National conference on recent advancement in Electronics, 2016 at IFHE, Hyderabad.
3. Ms Chandana, Dr V Ayyem Pillai, Mr GVS Reddy Analyses of SMI Algorithm for Smart Antenna Applications Jan 22, 23 2016, National conference on recent advancement in Electronics, 2016 at IFHE, Hyderabad.
4. Mr GVS Reddy, Dr V Ayyem Pillai Simulation and analysis of Sample matrix inversion algorithm for smart antenna application, Two-day national conference on wireless communication system , 27, 28 jan 2016, MITS, Madanapalle

Dr. G.Mamatha, Professor.

Qualifications: BE from Mysore University(1991), MS from BITS Pilani(1995), Ph.D (Full time) from IIIT Hyderabad(2013)

Experience: 18 years(Teaching), 6 years(Research)

Research Interest: VLSI, Signal Processing

Journal Publications/ Conference Proceedings: 10



Journal Publications

International Journals :

1. Mamatha Samson, Madhulatha "Energy Efficiency Enhancement for 45nm 1Mb SRAM Array Structures" International Journal of Computer Applications VOL 105 No 5, November 2014, pp 16-20
2. Keerthi .V, Mamatha Samson "Design of BIST with Low Power Test Pattern Generator" IOSR Journal of VLSI and Signal Processing Volume 4, Issue 5, Ver. II (Sep-Oct. 2014), pp 30-39
3. Mamatha Samson "Effect of Phase of Noise on 6T SRAM cell", IOSR Journal of VLSI and Signal Processing Volume 4, Issue 3, Ver. I (May-Jun. 2014), pp 30-38
4. Mamatha Samson Stable and Low Power 6T SRAM International Journal of Computer Applications VOL 78 NO 2 September 2013 pp 6 -10
5. Mamatha Samson, "Static Performance Analysis of Low power SRAM" IJCSNS International Journal of Computer Science and Network Security, VOL.10 No.5, May 2010, pp 189-195
6. K. Supriya, Dr. G. Mamatha, "Implementation of optimal Encoder Architecture for long Polar Codes", IOSR Journal of VLSI and Signal Processing, Vol 6, Issue 6, Nov – Dec 2016, PP 87-95
7. Ramagoni Swapnika, Dr. G. Mamatha, "Content Addressable Memory Architecture based on Sparse Clustered Networks", IOSR Journal of VLSI and Signal Processing, Vol 6, Issue 6, Nov – Dec 2016, PP 21-28

Conference Proceedings

International Conferences:

1. Mamatha Samson, Satyam MandaValli "Adiabatic 5T SRAM" International Symposium on Electronic System Design (ISED) 2011 (IEEE Co sponsored) (19-21 December 2011, Kochi, India) .
2. Mamatha Samson, "Performance Analysis of Dual Vt Asymmetric SRAM -Effect of Process Induced Vt Variations" International Conference on Advances in Computing, Control, and Telecommunication Technologies, ACT2009 December 28-29, 2009, Trivandrum, Kerala, India.
3. Mamatha Samson, M.B Srinivas "Analysis of faults of drowsy SRAM cell considering the effect of process variation." VLSI Circuits and Systems- SPIE EUROPE Micro technologies for the New Millennium 4 - 6 May 2009, Dresden, Germany.
4. Mamatha Samson, M.B Srinivas "Analyzing N-Curve Metrics for Sub-Threshold 65nm CMOS SRAM" proceedings of 8th IEEE Conference on Nanotechnology, 2008. (Nano'08) pp 25-28, (2008) 18-21 August, Arlington, Texas, U.S.A
5. Mamatha Samson, M.B Srinivas "Read Stability and Write Ability Analysis of Dual-Vt Configurations of a single Cell of an SRAM Array-Effect of Process-Induced Intra-Die Vt Variations" proceedings of 2ND IEEE International Nanoelectronics Conference (INEC008) pp 1015-1019, (2008) 24-27 March, Shanghai, China.

Dr. Usha Kumari Chintalapati , Professor.

Qualifications: B.Tech (ECE) 2003, M.Tech (Radar and microwave Engineering) AU-2005, Ph.D (JNTUH-2015)

Experience: 11.6 years

Research Interest: Wireless Communications

Journal Publications/Conference Proceedings: 18



Journal Publications:

International Publications:

1. "Investigation of GDOP for Precise user Position Computation with all Satellites in view and Optimum four Satellite Configurations": J. Ind. Geophys. Union , Vol.13, No.3, pp.107-116, July 2009
2. "Erlang Capacity Evaluation in GSM and CDMA cellular systems" : International Journal of Mobile Networks Communication & Telematics (IJMNCT), Volume 2, No. 5, pp. 25-32, October 2012
3. "Modelling of WCDMA Base Signal in Multipath Environment", International Journal of Application or Innovation in Engineering & Management (IJAIEEM), Volume 3, Issue 3, pp.324-330, March 2014
4. "Spectrum Sensing Based on Enhanced Energy Detector in Cognitive Radio Communications", International Journal of Scientific Engineering and Technology Research (IJSETR), Volume 4, Issue 2, pp.0195-0198, January 2015.
5. "Estimation of Bit Error Rate and Mean Square Error in 4G Wireless Networks", International Journal of Enhanced Research in Science Technology & Engineering (IJERSTE), Volume 4, Issue 3, pp.208-215, March 15.

International Conference Proceedings:

1. "Design and Implementation of Trellis Coded Modulation Scheme to Improve the BER of GPS Signal" in 2009 International Symposium on GPS/GNSS November 4~6, 2009 ICC Jeju, Korea.
2. "GPS Receiver Position by Least Square Approximation Technique", in 2010 International Conference on Mobile Internet Devices December 17~18, 2010, at Gokaraju Rangaraju Institute of Engineering Technology Hyderabad.
3. "Next Generation Heterogeneous Wireless Networks Handover Probability Analysis Based on Equal Bandwidth Allocation", in 2010 International Conference on Mobile Internet Devices December 17~18, 2010, at Gokaraju Rangaraju Institute of Engineering Technology, Hyderabad.
4. "Estimation of Path Loss in the Received Signal under Different Path Loss models in 4G Systems", in 2016 IEEE Sponsored International conference on Innovations in Information, Embedded and Communication Systems (ICIIECS 16), March 17~18, 2016, at Karpagam College of Engineering, Coimbatore, Tamil Nadu.

National Conference Proceedings:

1. "Side Lobe Reduction for Electromagnetic Compatibility using Arrays" in National Conference on Emerging And Converging Communication Technologies (SECT-07) on February 10~11, 2007, at ANDHRA UNIVERSITY, Visakhapatnam.
2. "Investigation of Path Loss and Shadowing Effects in Mobile Radio Channel" in National Conference on Advances in Communication Technologies (NCACT'12) on January 9~10, 2012, at GITAM Institute of Technology, GITAM UNIVERSITY, Visakhapatnam.

3. "Coverage and Capacity Prediction for WCDMA Air Interface Technology in the GVMC" in National Conference on Advances in Communication Technologies (NCACT'12) on January 9~10, 2012, at GITAM Institute of Technology, GITAM UNIVERSITY, Visakhapatnam.
4. "Investigation of Performance Analysis on Capacity for UMTS Cellular Networks" in conference on Advances in Communication, Navigation & Computer Networks (ACNCN-2012) on March 17~18, 2012, at Department of ECE, Andhra University College of Engineering (A), Visakhapatnam.
5. "Mobile Radio Channel Propagation Modelling for WCDMA Technology" in conference on Advances in Communication, Navigation & Computer Networks (ACNCN-2012) on March 17~18, 2012, at Department of ECE, Andhra University College of Engineering (AU), Visakhapatnam.
6. "Investigation of Capacity parameters for a 3G WCDMA Cellular Network" in national conference on Emerging Trends in Electronics and Communication Technologies, (NCECT'12) on 28th March 2012, at Department of ECE, JNTU College of Engineering (Autonomous), JNTU Anantapur.
7. "Propagation Characteristics of a Mobile Radio Channel in rural, Urban and Suburban areas for WCDMA Technology" in national conference on Emerging Trends in Electronics and Communication Technologies, (NCECT'12) on 28th March 2012, at Department of ECE, JNTU College of Engineering (Autonomous), JNTU Anantapur.
8. "Performance Analysis of GSM Cellular Network Traffic Capacity with Co-channel Interference", IETE Conference on Advanced Communications and Computer Technologies(ACCT-2012), July 6~7, 2012, NSTL Visakhapatnam.
9. "Evaluation of Erlang Capacity for CDMA and Wideband CDMA Cellular Systems", IETE Conference on Advanced Communications and Computer Technologies (ACCT-2012), July 6~7, 2012, NSTL Visakhapatnam.

V. Aravind, Professor.

Qualifications: Pursuing Ph.D

B.E. (ECE) (Thapar Institute of Engg & Technology, Patiala, 1988),
M.S. (EE) Northern Illinois University, IL, USA, 2000)

Experience: 12(Teaching) 14 (Industry)

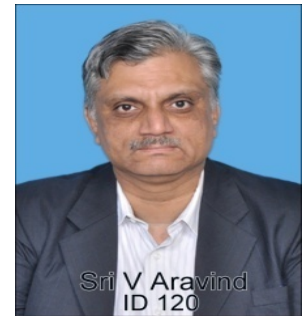
Research Interest: Signal Processing, Image Processing, Multimedia,
Embedded systems, Radio Frequencies

Journal Publications/ Conference Proceedings: 2

Journal Publications:

International Publications

1. Hand-held Unit for Imaging ISSN: 2249-8958, International Journal of Engineering & Advanced technology
2. Ground Station Design, ISSN:2277-3878, International Journal of Recent Technology and Engineering



Dr. D. Jayanthi, Professor.

Qualifications: B.E (ECE) 1993, M.Tech (VLSI Design) 2002,
Ph.D (Anna University, 2013)

Experience: 18. 9 Years

Research Interest: VLSI Design (Low power Asynchronization)

Journal Publications/Conference Proceedings: 16

Journal Publications:

International Publications:

1. Jayanthi. D. and Rajaram. M., “Design and Analysis of Asynchronous Pipeline Controller”- “European Journal of Scientific Research” ISSN 1450-216X Vol.72 No.1 (2012), pp. 84-91
2. Jayanthi. D. and Rajaram. M., “A Quasi Delay Insensitive Reduced Stack Pre-Charged Half Buffer based High Speed Adder using pipeline templates for Asynchronous Circuits” Journal of Computer Science 8 (7): 1114-1122, 2012, ISSN 1549-3636, pp. 1114-11122.
3. Jayanthi. D. and Rajaram. M., “The Design of High Performance Asynchronous Pipelines with Quasi Delay Insensitive” International Journal of Computer Applications , Volume 52– No.16, pp35-41., August 2012
4. Jayanthi. D. and Rajaram. M., “High Performance Asynchronous pipelined QDI templates for DCT Matrix-vector Multiplication” International Journal of Computer Applications (0975 – 8887) Volume 55– No.17, pp44-51., October 2012
5. D.Jayanthi “Logical Effort for SCV-Based Ripple Carry Adder” International Journal of Applied Engineering Research, ISSN 0973-4562 Vol. 10 No.55 PP 81-86, 2015.



Dr. K Padmavathi, Professor.

Qualifications: Ph.D(Nagarjuna University, 2016), BE from Andhra University(2000),M.Tech from JNTUA(2005)

Experience: 14 years

Research Interest: Signal Processing

Journal Publications/ Conference Proceedings: 29

Text book:

1. Padmavathi Kora, “ ECG based cardiac Arrhythmia Detection”, Lambert Academic Publishing, ISBN no: 978-3-330-02663-6

Journal Publications:

International Publications:

1. Padmavathi Kora, “ECG based Atrial Fibrillation Detection using Cuckoo Search Algorithm”, pp. 37-42, International Journal of Computer Applications, *Volume 162 – No 10*, Mar 2017, ISSN: 0975 – 8887
2. Padmavathi Kora, “Crossover Operators in Genetic Algorithms: A Review”, pp. 34-36, International Journal of Computer Applications, *Volume 162 – No 10*, Mar 2017, ISSN: 0975 – 8887
3. Padmavathi Kora, “ Performance improvement of MIMO using CS – SCHAT”, International Journal of Advanced Research, *ISSN: 2320-5407, Vol 4, issue 12, PP: 1601-1607*
4. Kora, Padmavathi. "Sudden Cardiac Death (SCD) Prediction based on Fast Sequency Ordered Complex Hadamard Transform." *International Journal of Computer Applications* 156.14 (2016).
5. Kora, P., Annavarapu, A., Yadlapalli, P., & Katragadda, N. (2016). Classification of Sleep Apnea using ECG-Signal Sequency Ordered Hadamard Transform Features. *International Journal of Computer Applications*, 156(14).
6. Padmavathi Kora, Ambika Annavarapu, “ECG – based Atrial Fibrillation Detection using different orderings of Conjugate Symmetric – Complex Hadamard Transform”, International Journal of Cardiovascular Academy, Vol 2, August, 2016
7. Padmavathi Kora, K. Sri Rama Krishna, " ECG Based Heart Arrhythmia Detection Using Wavelet Coherence and Bat Algorithm" *Sensing and Imaging*, Springer, Vol 17, no. 1, Jun 2016. DOI: 10.1007/s11220-016-0136-5.
8. Padmavathi Kora, "Bundle Block detection using Differential Evolution and Levenberg Marquardt Neural Network," *Pakistan Journal of Biotechnology*. Vol 13, Jun 2016. (SCOPUS)
9. Padmavathi Kora, K. Sri Rama Krishna, "Adaptive Bacterial Forging Optimization for the detection of Bundle Branch Block," *Egyptian Informatics Journal*, Elsevier, Vol 17, June 2016. (SCI Expanded, Impact Factor: 0.810). DOI: 10.1016/j.eij.2016.04.004.
10. Padmavathi Kora, K. Sri Rama Krishna, "Hybrid Firefly and Particle Swarm Optimization algorithm for the detection of Bundle Branch Block," *International Journal of Cardiovascular Academy*, Elsevier, Dec 2015. doi: 10.1016/j.ijcac.2015.12.001.
11. Padmavathi Kora, and Sri Ramakrishna Kalva, "Improved Bat algorithm for the detection of myocardial infarction," *SpringerPlus*, Springer, vol 4, no. 1, pp. 1-18, Nov 2015. (SCI Expanded).). 0.981 Impact Factor.
12. Padmavathi Kora, and Sri Rama Krishna, "Hybrid Bacterial Foraging and Particle Swarm Optimization for detecting Bundle Branch Block," *SpringerPlus*, Springer, vol 4, no 1, 481, Sep 2015. (SCI Expanded).
13. Padmavathi Kora, K. Sri Rama Krishna, "Classification of Bundle Branch using Magnitude Squared Coherence", *ARNP Journal of Engineering and Applied Sciences*, vol 10, no 14, pp. 5984-5989, Aug 2015. (Scopus indexed)
14. Padmavathi Kora, K. Sri Rama Krishna, "Detection of Atrial Fibrillation using Continuous



- Wavelet transform and Wavelet Coherence,” International Journal of Systems Control and Communications (IJSCC), Inderscience Publisher, vol 6, no 4, pp.292 - 304, 2015. (Scopus indexed).
15. Padmavathi Kora, K. Sri Rama Krishna, “Detection of Atrial Fibrillation using Autoregressive modeling” International Journal of Electrical and Computer Engineering (IJECE),” vol. 5, no.1, pp. 64-70, Feb 2015. (Scopus indexed)
 16. Padmavathi Kora, K. Sri Rama Krishna, “Myocardial Infarction detection using DSKC6713,” International Journal of Applied Engineering Research (IJAER), vol 10, no.1, pp. 2137-2148, Jan 2015. (Scopus indexed).

Conference Proceedings

International Proceedings

1. Padmavathi Kora, V. Ayyem Pillai, “Detection of Atrial Fibrillation using Cuckoo search algorithm”, International conference on Systemics, Cybernetics and Informatics, ICSCI-March 2017, Pentagram Research Centre, Hyderabad
2. V. Ayyem Pillai, Padmavathi Kora, “A Printed Planer Elliptical Monopole Antenna for Ultra wideband Application”, International conference on Systemics, Cybernetics and Informatics, ICSCI-March 2017, Pentagram Research Centre, Hyderabad.
3. Padmavathi Kora, Ambika Annavarapu, “Cognitive Recognition of Heart Ailments using Fuzzy Logic on ECG samples”, INTERNATIONAL CONFERENCE ON SCIENCE AND TECHNOLOGY FOR SUSTAINABLE DEVELOPMENT ICSTSD- April 2017, Bharat Institute of Engineering & Technology, Hyderabad, India.
4. Nagaja Katragadda, Ambika Annavarapu, Priyanka Yadlapalli, Padmavathi Kora, “Novel Hamming code detection and correction”, ICET – 2017, Coimbatore, December – 16th and 17th
5. Priyanka Yadlapalli , Ambika Annavarapu, Padmavathi Kora, Nagaja Katragadda, “Review of Various Crossover Operators in Genetic Algorithms”, ICET – 2017, Coimbatore, December – 16th and 17th
6. Priyanka Yadlapalli, Ambika Annavarapu, Padmavathi Kora, "Performance enhancement of CS – SCS based MIMO using Alamouti Scheme, " ICIECS 16, IEEE, Coimbatore, Mar 2016.
7. Padmavathi Kora, K. Sri Rama Krishna, “Bundle Block Detection using Genetic Neural Network,” International System design and Intelligent Applications (INDIA), AISC, Springer, Jan 2016.
8. Padmavathi Kora, K. Sri Ramakrishna, ”Detection Bundle Branch Block using Firefly Algorithm,” 6th joint International Conference on Swarm, Evolutionary and Memetic Computing (SEMCCO 2015), Hyderabad, INDIA, LNCS, Springer, DEC 2015.
9. Padmavathi Kora, K. Sri Rama Krishna, ”Detection of Bundle Branch Block using Bat Algorithm and Levenberg Marquardt Neural Network,” ICT for Intelligent Systems (ICTIS), Ahmedabad, INDIA, SIST, Springer Verlag, Nov 2015.
10. Padmavathi Kora, K. Sri Rama Krishna, “Bundle Block detection using Bat algorithm,” National Conference on Innovations and Design Challenges in Electrical and Medical Electronics (NCIDCEME), GRIET, Aug 2015.
11. K. Padmavathi, K. Sri Rama Krishna, “Classification of ECG signal during Atrial Fibrillation using Autoregressive modeling,” International conference on Information and Communication Technologies, Elsevier Procedia Computer Science, Kerala University of Science and Technology, Kerala, December 2014.
12. Padmavathi K, and Sri Rama Krishna K, “Myocardial infarction detection using magnitude squared coherence and Support Vector Machine,” Conference on Medical Imaging m-Health and Emerging Communication Systems (MedCom), International, pp. 382-385, IEEE, 2014.

Dr. K Swaraja, Professor.

Qualifications: Pursuing Ph.D

B.Tech (ECE) (JNTUA,2001), M.Tech (ECE) ((JNTUH,2006),

Experience: 15 yrs Teaching

Research Interest: Image and Video processing, Communications

Journal Publications/ Conference Proceedings: 7

Journal publications:

International publications

1. K.Swaraja, Y.Madhavvelatha and V.S.K.Reddy, "The Imperceptible Video Watermarking based on region of motion vectors in P-Frames", International Journal of Advances in Computational Sciences and Technology. ISSN 0973-6107 Volume 3, Number 3 (2010) pp. 335-348.
2. K.Swaraja, Y.Madhavvelatha, V.S.K.Reddy, "A secure method of optimized low complexity video watermarking", ARPN Journal of Engineering and Applied Sciences, VOL. 10, NO. 4, MARCH 2015, ISSN 1819-6608, PP.1822-1827.

Conference Proceedings:

National Proceedings

1. K. Swaraja, Y. Madhavvelatha and V.S.K.Reddy "Different Video Watermarking Techniques and Comparative analysis: A Survey", in Proc. Nat. Conf. Signal Processing & Communications, SPCOM, 2009, pp. ,Dec 4-5, 2009, Malla Reddy College of Engineering and Technology, Hyderabad.
2. K. Swaraja, Y. Madhavvelatha and V.S.K.Reddy "Video Watermarking in motion vector using Temporal Synchronization", in Proc. Nat. Conf. Signal Processing & Communications, SPCOM, Dec 23-24, 2010, pp.185-190, Malla Reddy College of Engineering and Technology, Hyderabad.
3. K. Swaraja, Y. Madhavvelatha and V.S.K.Reddy "Video Watermarking in motion vector based on visual masking", in Proc. Nat. Conf. Signal Processing & Communication system design, Feb 3-4, 2014, pp., Malla Reddy Engineering College for Women, Hyderabad.

International Proceedings:

1. K.Swaraja, Y.Madhavvelatha, V.S.K Reddy," video watermarking based on motion vectors of H.264", India conference (INDICON), 2011, Annual IEEE Conference on Engineering Sustainable Solutions, Dec 16-18, 2011 on page(s), 1-4, Hyderabad, India, ISBN: 978-1-4577-1110-7.
2. K.Swaraja, Y.Madhavvelatha, V.S.K.Reddy, "A Prestine Digital Video Watermarking in H.264 Compressed domain", IEEE International conference on computational intelligence and computing research, Dec 18-20, 2014, ICCIC1400521



Mr M. Kiran, Assoc. Professor.

Qualifications: B.Tech (ECE) (Nagarjuna University, 2001), M.Tech (C&C) (JNTU-H, 2007)

Experience: 12 yrs

Research Interest: Computers and Communications, Networking

Journal Publications/ Conference Proceedings: 4



Conference Proceedings:

International Proceedings

1. R.Sirisha , K.Jamal , M.Kiran “Programmable Pseudorandom Test Pattern Generator For BIST Implementation” IOSR Journal of VLSI and Signal Processing (IOSR-JVSP) Volume 6, Issue 6, Ver. III (Nov. - Dec. 2016), PP 58-63.
2. CH. Yagnasri, M. Kiran, K. Jamal “Implementation of FM0 and Manchester Encoder for Efficient Hardware Utilization” IOSR Journal of VLSI and Signal Processing (IOSR-JVSP) Volume 6, Issue 6, Ver. III (Nov. - Dec. 2016), PP 12-20.
3. L.Sujitha, M.Kiran, “LUT Design using OMS Technique for memory Based realization of FIR Filter”, IJERT, 9/1/2014, Vol-2 (6).
4. Pavan Kumar, M.O.V.; Kiran, M., "Design of optimal fast adder," Advanced Computing and Communication Systems (ICACCS), 2013 International Conference on, vol., no., pp.1,4, 19-21 Dec. 2013,Publisher:IEEE doi: 10.1109/ICACCS.2013.6938692

Ms K Meenakshi, Assoc. Professor.

Qualifications: Pursuing Ph.D (JNTUK), M.Tech (ECE) (JNTUH, 2005), B.Tech (ECE) (Nagarjuna University, 1994)

Experience: 15 yrs

Research Interest: *Digital image processing, VLSI Design*

Journal Publications/ Conference Proceedings: 6



Journal publications:

International publications

1. K Meenakshi, C Srinivasa Rao, K Satya Prasad , “A Scene Based Video Watermarking Using Slant Transform”, IETE Journal of Research 60 (4), 276-287, 2014
2. K Meenakshi, CS Rao, KS Prasad, “A Fast and Robust Hybrid Watermarking Scheme Based on Schur and Svd Transform”, International Journal of Research in Engineering and Technology 3 (4), 7-11, December, 2014
3. KSP K.Meenakshi, Ch.Srinivasa Rao, “A Hybridized Robust Watermarking Scheme based on Fast Walsh-Hadamard Transform and Singular Value Decomposition using Genetic Algorithm”, International journal of computer Applications 108 (11), 1-8, 2014

Conference Proceedings:

International Proceedings:

1. K Meenakshi, CS Rao, KS Prasad, “A robust watermarking scheme based Walsh-Hadamard transform and SVD using ZIG ZAG scanning”, International Conference on Information Technology (ICIT), 2014, 167-172, 2014
2. K Meenakshi, GNB Bethel, “Design and simulation of constant bit rate compressor using fuzzy logic”, First International Conference on Networks & Soft Computing (ICNSC) 2014.
3. Networks & Soft Computing (ICNSC), 2014 First International Conference on, “Robust Video Watermarking Schemes in Phase domain Using Binary Phase Shift Keying”, arXiv preprint arXiv:1404.1314, 2014

Ms N Swetha, Assoc. Professor.

Qualifications: Pursuing Ph.D

B.Tech (ECE) (JNTU-H, 2003), M.Tech (ECE) (JNTU-H, 2009)

Experience: 12 yrs

Research Interest: Embedded Systems

Journal Publications/ Conference Proceedings: 8

Journal publications:

International publications



1. Swetha N, Sastry P N, Rao YR, Sabat SL. "Parzen window entropy based spectrum sensing in cognitive radio. Computers & Electrical Engineering, Elsevier" 2016 Feb 26, [doi:10.1016/j.compeleceng.2016.02.002](https://doi.org/10.1016/j.compeleceng.2016.02.002).
2. **Namburu Swetha**, Panyam N, Yandra R, Sabat S," Fast Sequency-Ordered Complex Hadamard Transform based Parzen Window Entropy detection for Spectrum Sensing in Cognitive Radio Networks", IEEE communication letters, Volume PP, Issue 69, 8 April 2016.
3. G. Vaidehi, N. Swetha, Panyam Narahari Sastry, " Entropy based Spectrum Sensing in Cognitive Radio Networks", IJARCCCE, Vol 4, Issue 11, November 2015
4. Raveena Raghunath, N. Swetha, "A Review of pathloss models of spectrum sensing in cognitive radio networks", International Journal of Engineering Research and Technology, Volume 4, Issue 4, April 2015.
5. N. Swetha, P. Narahari Sastry, Y. Rajasree Rao,,: "Spectrum Sensing Analysis using PSD based Entropy Detection of DVB-T signal", ARPJ Journal of Engineering and Applied Sciences, Volume 9, No. 12, December 2014.
6. S. Sahithya, N.Swetha, "Real Time Vehicle Tracking System using GPS and GPRS", International journal of Research in Computer and Communication Technology, Vol 3, Issue 10, October 2014

Conference Proceedings:

International Proceedings

1. **Mrs. N.Swetha** attended a conference titled "FICTA 2016: International Conference on Frontiers of Intelligent Computing: Theory and Applications" and presented the paper titled "Performance analysis of Compressed Sensing in Cognitive Radio Networks" (under publishing).
2. Acoustic Emission Sensing Using Zigbee and Mobile Internet Devices(Mids) for Identifying Structural Damages ICMID 2010

Mr G.V.Subba Reddy Assoc. Professor.

Qualifications: B.Tech (ECE) -2002, M.Tech (VLSI) (JNTU- 2007)

Experience: 13 yrs

Research Interest: VLSI, Signal Image & Video Processing.

Journal Publications/ Conference Proceedings: 8



Journal publications:

International publications

1. “A Study of Sample Matrix Inversion Algorithm for Smart Antenna Applications”, G.V.Subba Reddy and V. Ayyem Pillai, Indian Journal of Science and Technology, Vol 9(15), DOI: 10.17485/ijst/2016/v9i15/88180, April 2016 , ISSN (Print) : 0974-6846 ,ISSN (Online) : 0974 5645.(scopus)
2. “An Analysis of Sample Matrix Algorithm for Smart Antenna Applications”, V.Ayyem Pillai, Sri Chandana, G.V.Subba reddy ,The IUP Journal of Telecommunications,vol VIII,no-2, ma 2016.
3. “Design and Implementation of 128X128 bit multiplier by Ancient Mathematics”-Goll Nagaraju ,G.V.Subba Reddy , In IJERT, Volume. 3, Issue- 09 , September – 2014, ISSN:2278 0181,ESRSA Publication.
4. “Power Optimised implementation of 32 bit carry look ahead adder using 16nm carbon nanotube technology” -N. Shashikanth, G.V.Subba Reddy,IJATIR, ISSN 2348–2370 Vol.07,Issue 14, October-2015, Pages:2844-2850.
5. “Detection of Soft Errors in 64-Bit Register Files using Self Immunity Technique”- Anjaneyul Borelli, G.V.Subba Reddy. IJATIR,ISSN 2348–2370 Vol.07,Issue.15, October-2015, Pages:2896 2901.

Conference Proceedings:

International Proceedings:

1. “Estimation of Path Loss in The Received Signal Under Lognormal Shadowing in 4G Systems” –G.V.Subba Reddy,Ch..Usha kumari,IEEE Sponsored 3 rd International Conference on Innovations in Information,Embedded and Communication Systems held on 17 th ,18 th march 2016.

National Proceedings:

1. “Analysis of Sample Matrix Algorithm for Smart Antenna Applications” 1 st National Conference on Recent Advancement in Electronics. V.Ayyem Pillai,K Sri Chandana, G.V.Subba reddy,in the NCRAE 2016 conducted at IFHE campus ,Hyderabad on January 22 nd - 23 rd 2016.
2. “Simulation and Analysis of Sample Matrix Inversion Algorithm for Smart Antenna Applications” -G.V.Subba Reddy and V.Ayyem Pillai , a two day (27 th & 28 th January 2016)National conference on Wireless Communication Systems ,at MITS Madanapalle,sponsored by DST –Science and Engineering Research Board,New Delhi.

Mr K. Jamal, Assoc. Professor .

Qualifications: Pursuing Ph.D (GITAM University),
M.Tech (ECE) (Bharath University, 2005), B.Tech (ECE) (JNTU, 2003)

Experience: Teaching : 8.3 yrs Industry : 3 yrs

Research Interest: VLSI

Journal Publications/ Conference Proceedings: 8



Journal publications:

International publications

1. K.Jamal, Dr.P.Sri hari “Low Power TPC using BSLFSR” *International Journal of Engineering and Technology* (IJET), Vol 8 No 2 Apr-May 2016. Page no.759.(Scopus Indexed)
2. S.Nayeema ,K.Jamal “Design and implementation of MAC Transmitter for the transmission of UDP packet using FSM and verilog coding techniques”, *IJERA*-Vol. 3, Issue 1, January -February 2013

Conference Proceedings:

1. K.Jamal, Dr.P.Sri hari “Test Vector Generation using Genetic Algorithm for Fault Tolerant Systems” 2nd International Conference on Human Computer Interactions (ICHCI-2016), Saveetha School of Engineering, Saveetha University, Chennai, TN.
2. K.Jamal, Dr.P.Sri hari “Analysis of Test Sequence Generators for Built-In Self-Test Implementation” 2nd International Conference on Advanced Computing and Communication Systems (ICACCS -2015, ISSN: 978-1-4799-6438-3/15/\$31.00 ©2015 IEEE) Jan. 05 – 07, 2015, Sri Eshwar College of Engineering, Coimbatore.
3. K.Jamal, Dr. P Sri hari “Pseudo Random Test Pattern Generation with CA, LFSR & GCG for BIST Implementation and their Comparison”-3rd International Conference on Innovation in Electronics and Communication Engineering (ICIECE-2014) - 18-19 July, 2014, Guru Nanak Institutions, Hyderabad. Proceeding Page No. 53
4. K.Jamal, Dr. P Sri hari “Pseudo Random Number Generation Using LFSR and Low Power LFSR” - IEEE Sponsored 2nd International Conference on Power, Control and Embedded Systems (ICPCES-2014) , February 27 – 28, 2014, Anna University, Chennai. Page No.103.

National Proceedings:

1. K.Jamal, IBK Raju “Optimized Implementation of RNS Arithmetic Based on FPGAs”- National Conference on Signals and Image Processing (NCSIP-2012)
2. K.Jamal “Implementations of low power multiplier in Digital FIR filter” -National Conference on Computing Communication and Instrumentation (NCCCI'12) at GITAM University, Hyderabad

Ms G Surekha, Associate. Professor

Qualifications: Pursuing Ph.D

B.Tech (ECE) (JNTU, Kakinada, 2004), M.Tech (ECE) (JNTUH, 2009)

Experience: 13 yrs

Research Interest: VLSI & Communications

Journal Publications/ Conference Proceedings: 2

International Journals:

1. “Design of Low Power 7T SRAM Cell Using Low-Vth Transistor” G.Surekha, Dr. N. Balaji, Dr. Y. Padma Sai, R. Prashanth Kumar International Journal of Advanced Research in Electronics and Communication Engineering (IJARECE) Vol., 4, Issue- 9, September 2015, Page No’s 2296-2302.

Conference Proceedings

1. “Stability Analysis of 7T-SRAM Cell” G. Surekha, Dr. N. Balaji, Dr. Y. Padma Sai 10th International Conference on Intelligent Systems and Control (ISCO 2016) ISCO Proceedings Vol.1, Page No’s.125-128, January 7-8, 2016. Karpagam College of Engineering, Coimbatore.



Ms D L Chaitanya, Assoc. Professor

Qualifications: Pursuing Ph.D

M. Tech (ECE) (JNTUH University, 2010)

B.Tech (ECE) (SKUniversity, 2005),

Experience: 10 yrs

Research Interest: VLSI

Journal Publications/ Conference Proceedings: 7

Journal publications:

International publications

1. **Chaitanya, D. Lakshmi**, and K. Manjunatha Chari. "Secure Channel Allocation Approach for Video Streaming in Cognitive Radio Networks." *Wireless Personal Communications, Springer*: 1-19.
2. B.Sowmya, Mrs. D.L.Chaitanya, "Upgraded MBIST with March-C algorithm testing towards RFID Memories", Proc. International Journal of Research in Electronics and Communications Engineering" 2(5), July-Aug. 2014.

National publications

1. D.L.Chaitanya, "A review of Different Spectrum Sensing Methods in Cognitive Radio Networks", PARIPEX-Indian Journal of Research, May 2015.

Conference Proceedings:

International Proceedings

1. D.L.Chaitanya, "Robust Centralized spectrum sensing in Cognitive Radio Networks" for LAMSYS 2016 June 2016 conducted by SDSC, SHAR, ISRO.
2. "Defense against PUEA and SSDF attacks in Cognitive Radio Networks", 3rd International conference ICIIECS '16 in Karpagam college of Engineering, Coimbatore
3. D.L.Chaitanya, "A review of Different Spectrum Sensing Methods in Cognitive Radio Networks" "International Conference on Innovations in Electronics and Communication Engineering (ICIECE-2014)" organized by ECE Dept., at Gurunanak Institutions, Hyderabad
4. D.L.Chaitanya, "Acoustic Emission Sensing Using Zigbee and Mobile Internet Devices(Mids) for Identifying Structural Damages" ICMID 2010



Dr. V Sree Hari, Associate Professor

Qualifications: Ph.D (IIITH, 2015), M.S (VLSI & ES, IIITH), B. Tech (Pondicherry University, 2004)

Experience: 12 yrs

Research Interest: High Speed Arithmetic Circuits, Memory Design, Low Power VLSI Design, Analog VLSI Design, Computer Architecture .Data Converters



Journal Publications/Conference Proceedings: 61

Journal publications:

International publications

1. Sandeep Saini ,Mahesh Kumar, Sreehari Veeramachaneni, M.B.Srinivas, "An Alternate approach to Buffer Insertion for Delay and Power Reduction in VLSI Interconnects", in the proceedings of the Journal of Low Power Electronics (JOLPE) vol. 6, No. 3 , pp 429-435 ,October 2010.
2. Mahesh Kumar, Sreehari Veeramachaneni, M.B.Srinivas, "Design of a Low Power Variable-Resolution Flash ADC", in the proceedings of the Journal of Low Power Electronics (JOLPE) vol. 5, No. 3 , pp 279-290, October 2009.
3. Sreehari Veeramachaneni, A. Mahesh and M.B. Srinivas, "A Balanced CMOS Logic style for Ultra Low Voltage/Low Power Operation", Communicated to IEEE Transaction on Circuits and Systems
4. Sreehari Veeramachaneni, M.B. Srinivas "Design of Efficient Arithmetic Circuits for Realization of Floating Point Adder/ Subtractor Units (Communicated to Microelectronics Journal – Elsevier)
5. Sreehari Veeramachaneni, M.B. Srinivas "Efficient Hardware Accelerator For Accurate Scientific And Numerical Computing. (Communicated to Microelectronics Journal – Elsevier)

Conference Proceedings:

International Proceedings:

1. Sreehari Veeramachaneni, M. B. Srinivas "Floating Point Adder/Subtractor Units Realization by Efficient Arithmetic Circuits" In the Proceedings of the 11th Conference on PhD Research in Microelectronics and Electronics (IEEE PRIME 2015) from 29th of June to 2nd of July 2015 at the University of Glasgow in Scotland.
2. B Naveen Kumar Reddy, Chandra Sekhar Mummidi, Sreehari Veeramachaneni and M B Srinivas. "A Novel Low Power Error Detection Logic for Inexact Leading Zero Anticipator in Floating Point Units", In the Proceedings of the 27th IEEE/ACM International Conference on VLSI Design and Embedded Systems (VLSI DESIGN -2014), Mumbai , India, 5th -9th January 2014.
3. Sai Phaneendra P, Chetan Vudadha, Sreehari Veeramachaneni, Srinivas M.B. "An Optimized Design of Reversible Quantum Comparator", In the Proceedings of the 27th IEEE/ACM International Conference on VLSI Design and Embedded Systems (VLSI DESIGN -2014), Mumbai , India, 5th -9th January 2014 .
4. Vudadha, Chetan; Phaneendra, P.Sai; Veeramachaneni, Sreehari; M.B. Srinivas, "A New Design of an N-Bit Reversible Arithmetic Logic Unit," in Fifth IEEE International Symposium on Electronic System Design (ISED), 2014 , vol., no., pp.224-225, 15-17 Dec. 2014.
5. Syed Ershad Ahmed, Pawan Sastry, Sreehari Veeramachaneni, M.B.Srinivas;, " A High Accuracy, Low Memory Logarithmic Converter ",Fourth IEEE International Symposium Highly-Efficient Accelerators and Reconfigurable Technologies (HEART) , proceedings., pp.121-124, 13-14 June 2013,Edinburgh ,Scotland.
6. Sreehari Veeramachaneni, M. B. Srinivas, "Design of Optimized Arithmetic Circuits for Multiplier Realization", The 2013 IEEE Asia Pacific Conference on Postgraduate Research in Microelectronics & Electronics (PrimeAsia), 2013, 19-21 December 2013.
7. Vaidya, A.S., Makkana, G, Sreehari Veeramachaneni, Srinivas, M.B,Rao, S.K. "A sustainable solution for monitoring malnutrition in children in developing countries" IEEE Global Humanitarian Technology Conference: South Asia Satellite (GHTC-SAS), 170 - 174 23-24 Aug. 2013 Trivandrum,India.

8. Sreehari Veeramachaneni, M. B. Srinivas, "Design of Efficient Arithmetic Circuits for Realization of Floating Point Adder/Subtractor Units", 2013 IEEE/IFIP 20th International Conference on VLSI and System-on-Chip (VLSI-SoC), 7-9 Oct. 2013, Istanbul, Turkey.
9. Vudadha, Chetan, Sai, Phaneendra P, Sreehari, Veeramachaneni, Srinivas, M B;,"CNFET based ternary magnitude comparator,"IEEE International Symposium on Communications and Information Technologies (ISCIT),2012, vol., no., pp.942-946, 2-5 Oct. 2012.
10. Vudadha, Chetan, Phaneendra, P. Sai, Sreehari, Veeramachaneni ., Ahmed, Syed Ershad; Muthukrishnan, N. Moorthy; Srinivas, M.B.; , "Design of Prefix-Based Optimal Reversible Comparator," IEEE Computer Society Annual Symposium on VLSI (ISVLSI),2012, vol., no., pp.201-206, 19-21 Aug. 2012.
11. Syed Ahmed, Sreehari Veeramachaneni, Srinivas M.B, Sibi Abhram and Moorthy Muthukrishnan. "A Modified TwinPrecision Multiplier with 2D bypassing technique" IEEE International Symposium on Electronic System Design (ISED), 2012, vol., no., pp. 102 - 106, 19-22 Dec. 2012
12. Vudadha, Chetan, Phaneendra, P. Sai Sreehari, Veeramachaneni . Ahmed, Syed Ershad, Muthukrishnan, N. Moorthy; Srinivas, M.B.; , "Design and Analysis of Reversible Ripple, Prefix and Prefix-Ripple Hybrid Adders," IEEE Computer Society Annual Symposium on VLSI (ISVLSI),2012, vol., no., pp.225-230, 19-21 Aug. 2012.
13. Vudadha, Chetan, Sreehari, Veeramachaneni . Srinivas, M. B.; , "Multiplexer Based Design for Ternary Logic Circuits," ,2012 IEEE 8th Conference on Ph.D. Research in Microelectronics and Electronics (PRIME), vol., no., pp.1-4, 12-15 June 2012.
14. Vudadha, C. Phaneendra P, S.; Makkena, G. Sreehari, Veeramachaneni.; Muthukrishnan, N.M.; Srinivas, M.B.; , "Design of CNFET based ternary comparator using grouping logic," 2012 IEEE Faible Tension Faible Consommation (FTFC),, vol., no., pp.1-4, 6-8 June 2012.
15. Vudadha, C.; Makkena, G.; Nayudu, M.V.S.; Phaneendra, P.S.; Ahmed, S.E.; Sreehari Veeramachaneni, .; Muthukrishnan, N.M.; Srinivas, M.B.; , "Low-Power Self Reconfigurable Multiplexer Based Decoder for Adaptive Resolution Flash ADCs," 2012 25th IEEE/ACM International Conference on VLSI Design (VLSID),, vol., no., pp.280-285, 7-11 Jan. 2012
16. Harshit Gulati, Shriyansh Vaishya, Sreehari Veeramachaneni "Bluetooth and Wi-Fi controlled rescue robots ",IEEE India Conference - INDICON , 2011.
17. V. Chetan, Phaneendra, P, Syed Ershad, SreehariVeeramachaneni, Muthukrishnan Moorthy, Srinivas, M.B, "A Reconfigurable INC/DEC/2's Complement/Priority Encoder Circuit with Improved Decision Block," IEEE International Symposium on Electronic System Design (ISED), 2011, vol., no., pp.100-105, 19-21 Dec. 2011.
18. Mahesh Kumar Adimulam Krishna Kumar Movva Sreehari Veeramachaneni N. Moorthy Muthukrishnan M.B. Srinivas "A Multiple-Bandwidth 10-bit SAR Analog to Digital Converter", IEEE International Symposium on Electronic System Design (ISED), 2011, vol., no., pp.100-105, 19-21 Dec. 2011.
19. Chetan Kumar, Sai Phaneendra, Ershad Ahmed, Sreehari Veeramachaneni, Moorthy Muthukrishnan, Srinivas M.B., "Higher radix sparse-2 adders with improved grouping technique," TENCON 2011 - 2011 IEEE Region 10 Conference , vol., no., pp.676-679, 21-24 Nov. 2011.
20. Phaneendra. P, Vudadha. C, Ahmed.S, Sreehari Veeramachaneni, Muthukrishnan, N, Srinivas, M.B., "Increment/decrement/2's complement/priority encoder circuit for varying operand lengths," 11th IEEE International Symposium on Communications and Information Technologies (ISCIT), 2011 , vol., no., pp.472-477, 12-14 Oct. 2011
21. Sai Phaneendra P, Sreehari Veeramachaneni, N Moorthy Muthukrishnan, M.B. Srinivas, "Conditional Sum Block for High Sparse Adders", The 2011 IEEE Asia Pacific Conference on Postgraduate Research in Microelectronics & Electronics (PrimeAsia), 2011, 6-8 October 2011. (GOLD LEAF Certificate).

22. Ahmed, Syed Ershad, Sreehari Veeramachaneni, N Moorthy Muthukrishnan, M.B. Srinivas, "Reconfigurable Adders for Binary/BCD addition/Subtraction", The 2011 IEEE Asia Pacific Conference on Postgraduate Research in Microelectronics & Electronics (PrimeAsia), 2011, 6-8 October .
23. Chetan Kumar, V. Sai Phaneendra, P. Ahmed, Syed Ershad, Veeramachaneni, Sreehari; Muthukrishnan, N. Moorthy; Srinivas, M.B.; , "A Unified Architecture for BCD and Binary Adder/Subtractor," Digital System Design (DSD), 2011 14th IEEE Euromicro Conference on , vol., no., pp.426-429, Aug. 31 2011-Sept. 2 2011.
24. Chetan Kumar V., Sai Phaneendra P., S. Ershad Ahmed, Sreehari Veeramachaneni, N. Moorthy Muthukrishnan, M. B. Srinivas: A Prefix Based Reconfigurable Adder, IEEE Computer Society Annual Symposium on VLSI (ISVLSI 2011), Chennai, India, July 4-6, 2011.
25. Chetan Vudadha, Sai Phaneendra, Syed Ershad Ahmed, Sreehari Veeramachaneni, Moorthy Muthukrishnan and Srinivas M.B "An Improved Sum Computation Block for adders with High Sparseness ", in 20th IEEE/ACM International Workshop on Logic & Synthesis (IWLS 2011), June 2011, San Diego, CA, USA.
26. Mahesh Kumar, Sreehari Veeramachaneni , N. Moorthy Muthukrishnan, M.B.Srinivas, "Low Power, Variable Resolution Pipelined Analog to Digital Converter with Sub Flash Architecture" In the Proceedings of the 11th IEEE Asia Pacific Conference on Circuits and Systems (APCCAS2010), December 6th -9th 2010 , Kuala Lumpur ,Malaysia.
27. Mahesh Kumar, Sreehari Veeramachaneni, N. Moorthy Muthukrishnan, M.B.Srinivas, "A Multiple-Bandwidth 12-bit Pipelined Analog to Digital Converter with Self-Clock Generator" In IEEE International Symposium on Communications and Information Technologies (ISCIT-2010, October 26-29 2010,), Meiji University, Tokyo, Japan.
28. Mahesh Kumar Adimulam , Sreehari Veeramachaneni , M.B.Srinivas, "Towards Realizing Variable Resolution Analog to Digital Converters" In the proceedings of the 2010 IEEE Asia Pacific Conference on Postgraduate Research in Microelectronics & Electronics (PRIME-ASIA 2010)September 22 – 24, 2010 ,Shanghai Olympic Hotel, China.
29. Mahesh Kumar Adimulam, Sreehari Veeramachaneni, N. Moorthy Muthukrishnan, M.B.Srinivas , " A Novel, Variable Resolution Flash ADCwith Sub Flash Architecture ", in the proceedings of IEEE Computer Society Annual Symposium on VLSI (ISVLSI-2010), July 5-6, 2010,lixouri,Kefalonia,Greece.
30. Mahesh Kumar Adimulam, Krishna Kumar Movva, Sreehari Veeramachaneni, N. Moorthy Muthukrishnan, M.B.Srinivas: "A low power, variable resolution two-step flash ADC". ACM Great Lakes Symposium on VLSI 2010 (GLSVLSI 2010): pp.39-44, Brown University Campus, Providence, Rhode Island, USA ,May 16-18, 2010.
31. P.Sai Phaneendra, Rakhee, Sreehari Veeramachaneni N.Moorthy Muthukrishnan, M.B.Srinivas,Patrick Kane." A Reconfigurable sensor network for disaster management", IEEE SENSOR 2010, November 1-4 2010, Hawaii, USA.
32. Sandeep Saini, A. Mahesh Kumar, Sreehari Veeramachaneni, M.B.Srinivas, "Alternative approach to Buffer Insertion for Delay and Power Reduction in VLSI Interconnects", Proceedings of the 23th IEEE/ACM International Conference on VLSI Design and Embedded Systems(VLSI DESIGN -2010), Bangalore, India, 3-7th January 2010.
33. Sandeep Saini, Sreehari Veeramachaneni, A. Mahesh Kumar, M. B. Srinivas, "Schmitt trigger as an alternative to buffer insertion for delay and power reduction in VLSI interconnects" IEEE Region 10 International Conference - TENCON , 2009.
34. Ronak Bajaj, Saransh Chhabra, Sreehari Veeramachaneni, M.B.Srinivas "A Novel, Low-Power Array Multiplier Architecture" In the proceedings of the 9th IEEE International Symposium on Communications and Information Technologies (ISCIT 2009) 28th-30th September 2009 in Incheon, Korea.
35. Mahesh Kumar, Sreehari Veeramachaneni , M.B.Srinivas, "A Novel Low Power, Variable Resolution Pipelined Analog to Digital Converter" ,In the proceedings of the 22nd IEEE International SOC conference (IEEESOCC 2009) 9th -11th September 2009 in Belfast, Northern Ireland, UK.

36. Mahesh Kumar, Sreehari Veeramachaneni , M.B.Srinivas, "Design of Low Power and High Speed Reconfigurable Resolution two step flash ADC" ,In the proceedings of the 1st IEEE Asia Symposium on Quality Electronic Design (ASQED 2009) 15-16 July 2009 in Kuala Lumpur, Malaysia.
37. Sreehari Veeramachaneni , M.B.Srinivas, "Redefining CMOS Logic Style for Subthreshold Operation" In the proceedings of the 5th IEEE International Conference on Ph.D. Research in Microelectronics and Electronics (PRIME'09) ,University College Cork, Ireland, 12-17 July.
38. Anshul Singh, Aman Gupta, Sreehari Veeramachaneni, M.B.Srinivas, "A High Performance Unified BCD and Binary Adder/Subtractor", In the proceedings of IEEE Computer Society Annual Symposium on VLSI (ISVLSI-2009), May 13-15, 2009, Tampa, Florida.
39. Sreehari Veeramachaneni, M.B.Srinivas "A New, Low-Power CMOS Logic Style for Nano-Meter Logic Circuits" In the Proceedings of NanoIsrael 2009, Jerusalem , Israel March 30-31, 2009.
40. Sreehari Veeramachaneni M.B.Srinivas , " Sensor Network tested for Disaster Monitoring", In the proceedings of the 8th IEEE Conference on SENSORS 2009 (IEEE SENSORS 2009) October 25 - 28, 2009 , Christchurch, New Zealand.
41. Sreehari Veeramachaneni, A. Mahesh Kumar , Venkat Tummala, M.B.Srinivas,"Design of a Low Power Variable-Resolution Flash ADC", In the Proceedings of the 22nd IEEE/ACM International Conference on VLSI Design and Embedded Systems (VLSI DESIGN -2009),New Delhi , India, 5th -9th January 2009.
42. Mahesh Kumar ,Sreehari Veeramachaneni, M.B.Srinivas, "Low Voltage High Performance Flash ADC", In 9th of the IEEE Asia Pacific Conference on Circuits and Systems (IEEE APCCAS 2008), Venetian Macao-Resort-Hotel, Macao, China November 30 - December 3, 2008.
43. Sreehari Veeramachaneni, M.B.Srinivas, "Novel High-Speed Architecture for 32-Bit Binary Coded Decimal (BCD) Multiplier",In IEEE International Symposium on Communications and Information Technologies (ISCIT-2008), Don Chanh Palace Hotel, Vientiane , Lao PDR, October 21-23,2008.
44. Sreehari Veeramachaneni, M.B.Srinivas, "New Improved 1-Bit Full Adder Cells", In 21st IEEE Canadian Conference on Electrical and Computer Engineering (CCECE-2008) Symposium on Circuits, Devices and Systems, Sheraton Fallsview Niagara Falls Ontario, Canada, May 4-7, 2008
45. Sreehari Veeramachaneni ,Kirthi Krishna M, Subroto Sen, Prateek G V, Bharat Sankhlecha, M.B.Srinivas, "A Novel Carry-look ahead approach to an Unified BCD and Binary Adder/Subtractor", In the Proceedings of the 21st IEEE/ACM International Conference on VLSI Design and Embedded Systems(VLSI DESIGN -2008), Hyderabad , India, 4th -8th January 2008 .
46. Sreehari Veeramachaneni ,Kirthi Krishna M, Lingamneni Avinash , Sreekanth Reddy P, M. B. Srinivas, " Efficient Design of a 32-Bit Comparator Using Carry Look-Ahead Logic" in Joint Conference on 50th IEEE Mid West Symposium on Circuits and Systems (MWSCAS-2007) and 5th North East Symposium on Circuits and Systems (NEWCAS-2007), August 5-8, 2007, Montreal, Canada.
47. Sreehari Veeramachaneni ,Kirthi Krishna M, Lingamneni Avinash , M B Srinivas, " A Novel High-Speed Binary and Gray Incrementer/Decrementer for an Address Generation Unit," In 2nd IEEE International Conference on Industrial and Information Systems (ICIIS 2007), 8-11, August 2007, University of Peradeniya, Srilanka.
48. Sreehari Veeramachaneni ,Kirthi Krishna M, Lingamneni Avinash , Sreekanth Reddy P,M.B.Srinivas "Novel, High-Speed 16-Digit BCD Adders Conforming to IEEE 754r Format",.: in the proceedings of IEEE Computer Society Annual Symposium on VLSI (ISVLSI-2007), May 9-11, 2007, Porto Alegre, Brazil.
49. Sreehari Veeramachaneni , Mayank Agarwal, Siddartha K, Rajashekhar Reddy M, Sri Harish Reddy M and M. B. Srinivas "New Full Adder Cells for Sub-threshold Operations"

- Proceedings of the 16th IEEE/ACM SIGDA International Workshop on Logic & Synthesis (IWLS-2007), May 30 - June 1, 2007, Paradise Point Resort & Spa San Diego, CA, USA. (Co-located with the (DAC) Design Automation Conference).
50. Sreehari Veeramachaneni, Lingamneni Avinash, Rajashekhar Reddy M, M.B.Srinivas, "Efficient Implementations of Residue to Binary Converters for $(2k-1, 2k, 2k+1)$ Moduli Set", In the 2007 World Congress in Computer Science, Computer Engineering, and Applied Computing (WORLDCOMP'07), Monte Carlo Resort, Las Vegas, Nevada, USA (June 25-28, 2007).
 51. Sreehari Veeramachaneni, Kirthi Krishna M, Lingamneni Avinash, Sreekanth Reddy P, M.B.Srinivas, "Novel High-Speed Redundant Binary to Binary Converter Using Prefix Networks" IEEE International Symposium on Circuits and Systems (ISCAS-2007) May 27-30, 2007, New Orleans, USA
 52. Sreehari Veeramachaneni, Lingamneni Avinash, Kirthi Krishna M, M.B.Srinivas, "Novel Architectures for Efficient (m, n) Parallel Counters" In 17th ACM/IEEE Great Lakes Symposium on VLSI (GLSVLSI-2007), Stresa-Lago Maggiore, Italy, March 11-13, 2007.
 53. Sreehari Veeramachaneni, Kirthi Krishna M, Lingamneni Avinash, Sreekanth Reddy P, M.B.Srinivas, "Novel Architectures for High-speed and Low-power 3-2, 4-2 and 5-2 Compressors" Proceedings of the 20th IEEE/ACM International Conference on VLSI Design and Embedded Systems (VLSI DESIGN -2007), Bangalore, India, 6-10th January 2007
 54. Sreehari Veeramachaneni, Lingamneni Avinash, Rajashekhar Reddy M, M.B.Srinivas "Efficient Modulo $(2k \pm 1)$ Binary to Residue Converters" Proceedings of the 6th IEEE International Workshop on SOC Real-Time Applications (IWSOC- 2006), Cairo, Egypt, December 26-28th 2006.
 55. Sreehari Veeramachaneni, Kirthi Krishna M, Lingamneni Avinash, Sreekanth Reddy P, M.B.Srinivas, "Design of a High Performance and Efficient Arithmetic Logic Unit (ALU)", In International Microelectronics & Packaging Society (IMAPS) India National Conference (IINC-2006) on Microelectronics & VLSI, Hyderabad, India, 8-10th December 2006. (Best paper award).
 56. Sreehari .Veeramachaneni, Pradeep Yarlagadda, and M. B. Srinivas "A Fully Multiplexer-based Implementation of Redundant Number System" Proceedings of the 15th IEEE/ACM SIGDA International Workshop on Logic & Synthesis (IWLS-2006), June 7-9, 2006, Vail, Colorado, USA.

Dr Arun Vignesh, Assoc. Professor

Qualifications: Ph.D (Anna University
M. E (Applied Electronics) (Anna University, 2011)
B.E (ECE) (Anna University, 2009),

Experience: 7 yrs

Research Interest: Wireless Networks

Journal Publications/ Conference Proceedings: 6

Journal publications:

International publications

- 1) N.Arun Vignesh and P.Poongodi, "Analysis of localized Quality of Service improvement architecture for wireless LAN", Wireless Personal Communications, Springer, vol. 86, no. 4, pp 1-11
- 2) N.Arun Vignesh and P.Poongodi, "Dynamic Performance of an Enhanced Cooperative Strategy for QoS Improvement In WLAN" International Journal of Computer Networks and Wireless Communications (IJCNCW), ISSN: 2250-3501 Vol.6, No 2, Mar-Apr 2016.
- 3) N.Arun Vignesh and P.Poongodi, "Improving the performance of Quality of Service using mobile node positioning algorithm in WLAN", Asian Journal of Information technology, Medwell Journals.
- 4) N.Arun Vignesh and P.Poongodi, "A Cluster Based Network Architecture Scheme for QoS Improvement in WLAN", International Journal of Networking and Virtual Organisations, Inderscience.
- 5) N.Arun Vignesh and Banupriya.S, "Vampire detection to improve quality of service in wireless networks", International Journal of Research in Electronics, ISSN: 2349-252X, Vol. 02, Issue-1, 2015.

Conference Proceedings:

International Proceedings:

1. 'Improving Security in delay constraint Wireless LAN' in **International conference**, IEEE Xplore held in Karpagam College of Engineering, Cbe-42.

National Proceedings:

1. 'Extraction of contact resistance in TFT' at the **National level conference** at Hindustan Engineering College
2. 'Generation of four phase pulse compression sequences using FPGA' at **National conference** in Kumaraguru College of Technology
3. 'Pulse Compression Sequences for Radar' in **National conference** at Surya Engineering College.



Mr N Madhusudhana Rao, Assistant Professor.

Qualifications: M. Tech(ECE) (JNTU CEH, 2012),
B.E. (ECE) (Anna University, 2006)

Experience: Teaching : **9.6 yrs** Industry : **1 yr**

Research Interest: Communication Systems, Electronics

Journal Publications/ Conference Proceedings: 2

Journal publications:

International publications

1. N. Madhusudhana Rao, “ Area – Delay Efficient Binary Address in QCA”, IJMETMR, Volume NO: 1 (2014), Issue No: 10 (October) ISSN :2348-4845
2. N. Madhusudhana Rao, “ Design and Development of Driver Behavioral Monitoring System”, IJSETR, Volume no: 4, Issue No: 45, ISSN :2319-8885, November – 2015



Mr M O V Pavan Kumar, Assistant Professor.

Qualifications: M.Sc. (Electronics) (Nagarjuna University, 2003),
M.Tech (ECE) (Satyabhama University, 2007)

Experience:9 yrs

Research Interest: VLSI, Analog/ Digital System Design

Journal Publications/ Conference Proceedings: 2

Journal publications:

International publications

1. B.Navya Teja, M.O.V.Pavan Kumar, "Architecture and Implementation of OFDM Transmitter and Receiver", IJSET, Vol. 3(9), Pg 2504-2507, Sep. 2014.
2. K. Sravani, M.O.V.Pavan Kumar, "Efficient technique of data hiding in encrypted images", International Journal of Science, Engineering and Technology Research, Vol 3, Issue 9, Sep 2014.

Conference Proceedings:

International Proceedings

1. Pavan Kumar, M.O.V.; Kiran, M., "Design of optimal fast adder," Advanced Computing and Communication Systems (ICACCS), 2013 International Conference on , vol., no., pp.1,4, 19-21 Dec. 2013,Publisher:IEEE doi: 10.1109/ICACCS.2013.6938692



Mr K N V Khasim, Assistant Professor.

Qualifications: B.Sc. (M.P.E.) (Nagarjuna University, 2005), M.Sc. (Electronics) (Andhra University, 2007), M.Tech(Communication Systems),(Andhra University,2012)

Experience: 6 yrs

Research Interest: Communication Systems, Embedded Systems

Journal Publications/ Conference Proceedings: 8

Journal publications:

International publications

1. "Automatic Irrigation system using a wireless sensor network and GPRS module", IJESRT, ISSN:2277-9655 (Impact factor:3.78), October 2015.
2. "Design of The CAN Bus Booster Pack and The Implementation of the CAN Protocol", International Journal of Research in Computer and Communication Technology, Vol 3, Issue 10, October – 2014.
3. Implementation of a Turbo Encoder and Turbo Decoder on DSP Processor-TMS320C6713(IJERD, Volume 2, Issue 5 July 2012)
4. Convolutional Code of Rate 2/3 to Approach the Theoretical Limit Imposed by Shannon's Channel Capacity(IJCST Vol. 2, SP 1, December 2011)

Conference Proceedings:

International Proceedings

1. Enhanced Image Transmission using Turbo Codes And 16QAM with Iterative De-Noising Algorithm(Joint International Conference on Swarm, Evolutionary & Memetic Computing (SEMCCO) and Fuzzy & Neural Computing Conference (FANCCO) - 2011)
2. Impulse Noise Detection and Filtering Based on Adaptive Weighted Median Filter(ACNCN-2012)
3. Pavement image Analysis Using Radon Transform(ACNCN-2012)



Ms K Nagaja, Assistant Professor.

Qualifications: M.Tech (ESD) (Bharat University, 2011) , B.E (ECE) (Periyar University, Tamil Nadu, 2003)

Experience: 11 yrs

Research Interest: Embedded Systems, Digital Electronics

Journal Publications/ Conference Proceedings: 4

Journal publications:

International publications

1. Kora, P., Annavarapu, A., Yadlapalli, P., & Katragadda, N. (2016). Classification of Sleep Apnea using ECG-Signal Sequence Ordered Hadamard Transform Features. *International Journal of Computer Applications*, 156(14).
2. K. Nagaja, "QR Images: Optimized Image Embedding in QR Codes", IJMETMR, ISSN : 2348-4845, 2015

Conference Proceedings:

1. Nagaja Katragadda, Ambika Annavarapu, Priyanka Yadlapalli, Padmavathi Kora, "Novel Hamming code detection and correction", ICET – 2017, Coimbatore, December – 16th and 17th
2. Priyanka Yadlapalli , Ambika Annavarapu, Padmavathi Kora, Nagaja Katragadda, "Review of Various Crossover Operators in Genetic Algorithms", ICET – 2017, Coimbatore, December – 16th and 17th



Ms V HimaBindu, Assistant Professor.

Qualifications: Pursuing Ph.D (GITAM University), M.E (TC) (RMIT University, Australia, 2005), B. Tech (ECE) (JNTU, 2002)

Experience: Teaching : 9.8 yrs Industry : 2 yrs

Research Interest: Wireless Communication

Journal Publications/ Conference Proceedings: 4

Journal publications:

International publications

1. HimaBindu Valiveti, "EHSD: An Exemplary Handover Scheme during D2D Communication based on Decentralization of SDN" in Wireless Personal Communications", Springer, July, 2016.
2. Valiveti. Hima Bindu, Vol 2, No 8 (2014): August - Articles Automatic Measuring And Recompense System For Monitoring Health Management Process For Automobiles

Conference Proceedings:

1. HimaBindu. Valiveti, titled "Hierarchy Knapsack approach for Network Selection in HetNets", 3rd International conference ICII ECS '16 in Karpagam college of Engineering, Coimbatore.
2. HimaBindu Valiveti, "D2D Handoff Mechanism based on SDN principles" for LAMSYS 2016 June 2016 conducted by SDSC, SHAR, ISRO.



Ms Ch Pratyusha Chowdari, Assistant Professor.

Qualifications: Pursuing Ph.D (GITAM University),
M.Tech (VLSI Design) (Andhra University, 2011), B. E (ECE) (Andhra University, 2009)

Experience: 6 yrs

Research Interest: VLSI & DSP

Journal Publications/ Conference Proceedings: 1

Conference Proceedings:

1. Ch. Pratyusha Chowdari, "Design of Low Power Adaptive Filter using Vedic Multiplier", "INDIA 2016"



Ms D V Prasanthi, Assistant Professor.

Qualifications: M.Tech (DECS)(JNTUK, 2011), B.Tech. (ECE) (Nagarjuna University, 2008)

Experience: 6 yrs

Research Interest: VLSI & Communication

Journal Publications/ Conference Proceedings: 1

Journal Publications:

International Publications:

1. "An Optimal Low Power Adaptive Filter Design for Noise Reduction" in International Journal of Science, Engineering and Technology Research, Vol.3.Issue...9. .September 2014, pp.2405. .2410.



Mr Md Javeed Mehdi, Assistant Professor.

Qualifications: M.Tech. (Embedded Systems)(JNTUH, 2012)
B.Tech. (E.C.E.) (JNTUH, 2002)

Experience: 10 yrs

Research Interest: *Wireless Sensors Network, Communication Protocols, Network Security, and Internet of Things.*

Journal Publications/ Conference Proceedings: 2

Journal Publications:

International Publications:

1. A. Usha Kiran, Mohd Javeed Mehdi, “Design and Analysis of Blow Fish and AES algorithm using VHDL”, IJVDCS, Vol. 02, Issue. 08, Oct 2014, Pages:0699-0702
2. B S Sai Sridhar, Mr Md. Javeed Mehdi, “ Wireless Sensor Networks for civil structure health monitoring using GPS and GSM module”, IJSEAT, Vol 02, Issue *, Aug – 2014



Ms A UshaSree, Assistant Professor.

Qualifications: M.Tech(Embedded Systems) (JNTUH,2008),
B.E(ECE) (Anna University,2005)

Experience: 9 yrs

Research Interest: *Communications, Embedded Systems.*

Journal Publications/ Conference Proceedings: 1

Journal Publications:

International Publications:

A. Usha Sree, “Embedded Web Server Based Laboratory Monitoring & Controlling System”, IJSETR, ISSN: 2319 – 8885, vol:40, October – 2015, Pages: 8727 – 8730



Ms B Shilpa, Assistant Professor.

Qualification: B.tech(2005), M.tech (2010)

Experience: 9 years

Research Interest: *RF*

Journal Publications/ Conference Proceedings: 1

Journal publications:

International publications

1. B.Navya, V.JyothiSri, B.Shilpa "Simulation of 6-DOF", SSRG International Journal of Electronics and Communication Engineering (SSRG - IJECE), V2(8),29-34 August 2015. ISSN:2348 - 8549.



Ms Y Priyanka, Assistant Professor.

Qualifications: B.Tech (ECE) (AU,2007), M.Tech(ECE) (JNTUH,2012)

Experience: Teaching: 5 yrs Industrial: 2 yrs

Research Interest: *Embedded Systems, Networking, Communications*

Journal Publications/ Conference Proceedings: 4

Journal Publications:

International Publications:

1. Annavarapu, Ambika, Padmavathi Kora, and Priyanka Yadlapalli. "AN ENHANCED SCHT BASED ALAMOUTI SCHEME OVER MULTIPATH CHANNELS." (2006).
2. Kora, Padmavathi, et al. "ECG based Atrial Fibrillation detection using Sequency Ordered Complex Hadamard Transform and Hybrid Firefly Algorithm." *Engineering Science and Technology, an International Journal* (2017).
3. Kora, Padmavathi, et al. "Classification of Sleep Apnea using ECG-Signal Sequency Ordered Hadamard Transform Features." *International Journal of Computer Applications* 156.14 (2016).

Conference Proceedings:

1. Priyanka Yadlapalli, Ambika Annavarupu, Padmavathi Kora, Nagaja Katragadda " Review of Various Crossover Operators in Genetic Algorithms " ICET 16, IEEE, Coimbatore, Dec 2016.
2. Nagaja Katragadda , Ambika Annavarupu, Padmavathi Kora, Priyanka Yadlapalli, " Review of Various Crossover Operators in Genetic Algorithms " ICET 16, IEEE, Coimbatore, Dec 2016.
3. Priyanka Yadlapalli, "Performance enhancement of CS – SCHT based MIMO using Alamouti Scheme", ICIIECS'16, IEEE Conference (Coimbatore)



Ms V Jyothi Sri, Assistant Professor.

Qualification:B.Tech(2009), M.Tech (2011)

Experience: 3 years

Research Interest: Communications, Image Processing

Journal Publications/ Conference Proceedings: 1

Journal publications:

International publications

1. B.Navya, V.JyothiSri, B.Shilpa "Simulation of 6-DOF", SSRG International Journal of Electronics and Communication Engineering (SSRG - IJECE), V2(8),29-34 August 2015. ISSN:2348 - 8549.



Ms Swathi Karumuri, Assistant Professor.

Qualification:

B.Tech(2008), M.Tech (2014)

Experience: 4 years

Research Interest: *VLSI*

Journal Publications/ Conference Proceedings: 1

Conference Proceedings:

1. Swathi Karumuri, "FPGA Implementation of Fault Tolerant Embedded RAM using BISR Technique", International Conference On Current Innovations In Engineering And Technology International Association Of Engineering & Technology For Skill Development, ISBN: 378 - 26 - 138420 – 5, Sept. 2014



Ms K Sravani, Assistant Professor.

Qualification: B.Tech (2012), M.Tech (2014)

Experience: 1.4 yrs

Research Interest: *VLSI*

Journal Publications/Conference Proceedings: 1

Journal publications:

International publications

1. "Efficient technique of data hiding in encrypted images", International Journal of Science, Engineering and Technology Research, Volume 3, Issue 9, September 2014, 2508-2510



Ms K Sarvani, Assistant Professor.

Qualification: B.Tech(2010), M.Tech(2014), JNTUH

Experience: 1.4 yrs

Research Interest: Biomedical imaging and sensing, bio medical area, sensor networks

Journal Publications/Conference Proceedings: 01

Journal publications:

International publications

1. "Fault coverage circuit with Bist architecture for efficient hardware utilization for testing applications", International journal for scientific research and development, Vol.3, Issue-5, 2015



Mr G Pradeep Reddy, Asst.Professor

Qualification: B.Tech (Electronics & Communication Engineering) 2007, JNTUH, Telangana, India, M.Tech (Communication Engineering) 2010, VIT University, Vellore, Tamilnadu.

Experience:8(Teaching),1(Research)

Research Interest: *Ultra Wide Band Communications*

Journal Publications/Conference Proceedings: 4

Journal publications:

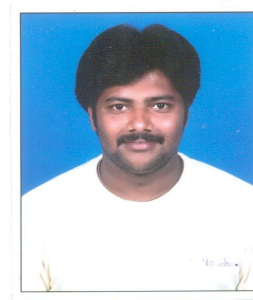
International publications

1. G.Nagaraju, Pradeep Reddy.G titled “Anatomization of Medical Image Segmentation with Novel Proposal” International Journal of Engineering and Computer Science, ISSN: 2319-7242 Volume 4 Issue 6, June 2015, Page No. 12766-12773.
2. O. Ravi Kumar, G. Pradeep Reddy, Gerardine Immaculate Mary titled “Source Localization Techniques for UWB Context Aware Applications” CIIT international journals, ISSN: 0974 – 9640, Vol 2 No 9, 2010, Page No.292-296.

Conferences Proceedings:

International Proceedings

1. B.Bharath, G.Pradeep reddy,et al.,”DIP Coated Thick Films of SnO₂ and It’s Ethanol Sensing Properties” was presented in the IEEE international conference on Applied Physics Mathematics (IEEE ICAPM 2011)(978-1-4244-9817-8)
2. G. Pradeep Reddy, Gerardine Immaculate Mary et al.,”UWB Indoor Channel profile identification with orthogonal hermite pulses” was presented in 3rd IEEE International Conference on Computer Science and Information Technology, 2010.(978-1-4244-5539-3)



Ms K P S Sravanthi, Assistant Professor

Qualifications: M.Tech(VLSI System Design)(JNTUK, 2013), B.Tech(ECE) (JNTUK,2010)

Experience: 5 yrs

Research Interest: *Digital Electronics, VLSI*

Journal Publications/Conference Proceedings:1

Journal publications:

International publications

1. KPS Sravanthi, “A Novel architecture for Digital Image Compression based on EBCOT Parallel processing”, IJSETR, Vol 2, Issue 19, Dec-2013, Page:2301-2312, ISSN:2319-8885



Ms K Gayathri, Assistant Professor

Qualifications: M.Tech(VLSI & ES)(JNTUA, 2013), B.Tech(ECE) (JNTUA,2000)

Experience: 4 yrs

Research Interest: *VLSI*

Journal Publications/Conference Proceedings: 1

Journal publications:

International publications

1. Gayathri, “DTMF Detection using recursive DFT on FPGA”, IJRA, Vol:3, Issue:2, April-2013.



Mr D Yesubabu, Assistant Professor

Qualifications: M.Tech(ES)(JNTUH, 2013), B.Tech(ECE) (ANU,2010)

Experience: 6 yrs

Research Interest: *Embedded Systems*

Journal Publications/Conference Proceedings: 1

Journal publications:

International publications

1. D. Yesu babu, “Multi Serial to Ethernet Gateway based on FPGA”, IJAIR, March – 2013



Mr R Prashanth Kumar, Assistant Professor

Qualifications: M.Tech(VLSI)(JNTUH, 2015), B.Tech(ECE) (JNTUH,2012)

Experience: 5 months

Research Interest: *VLSI, communications*

Journal Publications/Conference Proceedings: 1

Journal publications:

International publications

1. R. Prasanth Kumar, "Design of Low power 7T SRAM Cell using low Vth transistor", IJARECE, July-2015



Ms V Sirisha, Assistant Professor

Qualifications: M.Tech(VLSI System Design)(JNTUH, 2015), B.Tech(ECE) (JNTUH,2013)

Experience: 9 months

Research Interest: *VLSI*

Journal Publications/Conference Proceedings: 1

Conference Proceedings:

International Proceedings:

1. FPGA Implementation of Test Vector Monitoring BIST Vector Architecture System, Springer, GITAM, Dec-2015

