Name and Designation: KANAKANALA NAGA VENKATA KHASIM, ASSISTANT PROFESSOR
Email ID: khasim.knv@griet.ac.in

Educational Details

Pursuing Ph.D (ECE),Sathyabama Institute of Science andTechnology(Deemed), Chennai, Tamilnadu.

M.Tech. (Communication Systems), Sir. CR Reddy College of Engineering (Andhara University), Andhra Pradesh (2010-2012)

Professional Background:
Assistant Professor (ECE), GRIET, Hyderabad, June 2008 to tilldate.
Administrative Experience:
1.Worked in the duties of JNTUH student data entry
2. Worked in the duties of AICTE student, faculty, and facilities data entry
3. Worked in the duties JNTUH Affiliation inspection.
4. Worked in the duties of UG admissions in various requirements.
5. Organizing member Hackathon (Challenge ACI).
6.Organized various workshops.
7. Acting as ECE Mentoring co-ordinator.
8. Organizing member Khelotsav (Sports Meet).
9. Attended NNAC different criteria duties.
10. Attended NBA filing work.
11. Student online Feedback work.
12. Served as ECE department GREEN CAMPUS coordinator
13. Organised JNTUH and AICTE fingerprint and other data collection
14. Study Material preparation/collection for finishing school
15. Library catalogue preparation
16. External Lab exams conduction
17. Internal exams conduction
18. Organising Graduation day 2018
Committee Work:
- Member College Mentoring committee since 2019.
- Member Pulse (GRIET annual cultural event)

Courses Taught:

- Electromagnetic Field and Waves
- Transmission Lines
- Antennas and Wave Propagation
- Optical Communications
- Microprocessor and Microcontroller
- Object Oriented Programming through JAVA
- Microwave Engineering &Digital Communications Lab
- Microwave & Optical Communication Lab
- Digital Image Processing Lab
- Communication Protocols Lab
- Object Oriented Programming Through Java Lab

PUBLICATIONS:
1. IJERD-Implementation of a Turbo Encoder and Turbo Decoder on DSP Processor-TMS320C6713
2. Joint International Conference FANCCO - 2011
3. IJCST Vol. 2, SP 1, December 2011-Convolutional Code of Rate 2/3 to Approach the Theoretical Limit Imposed by Shannon's Channel Capacity
4. IRCCT-Design of The CAN Bus Booster Pack and The Implementation of The CAN Protocol on the TIVA C Launch pad Evaluation Board
5. IJESRT-AUTOMATIC IRRIGATION SYSTEM USING A WIRELESS SENSOR NETWORK AND GPRS MODULE

Review Work: No

Reviewer for Journals and Books: No

PROFESSIONAL WORK:
Talks/Paper Presentations: Served as resource person for one session in two basic level LabVIEW training to students of II&III B. Tech ECE, GRIET.

Seminars and Symposiums:

- Coordinator: FDP on " Recent trends in Computer Architecture, VLSI and Embedded Systems (RCAVES-2020)" from June 8 to June 13, 2020
- Coordinator: Alumni distinguished lecture on "Inside high performance SSDs" on June 12, 2020.
- Coordinator: Webinar on " Analog VLSI Design Flow: Specs to Final Product from Industry Perspective" on June 7, 2020.
- Coordinator: Webinar on "AI and ML approaches in Speech Systems" on June 6, 2020.
- Coordinator: Webinar on "SYNTHESIZABLE SUBSET OF VERILOG " on May 29, 2020.
- Coordinator: One Day Workshop on Arduino, a Course in the IoT Series
- Attended Electromagnetics and Microwave Engineering: Pedagogy, Research Trends and Applications (EMPRA2020)
- Attended Applications of Deep Learning for Signal and Image Processing Using Python.
- Challenge ACI 2019 Hackathon
- 1 Day workshop on Moodle Learning Management System
- 3day Training on Reconfigurable USRP Software Defined Radio Devices with LabVIEW & FPGA Platforms
- Moodle learning management system
- Hands-on Workshop on myDAQ and myRIO with LabVIEW Platform
- Participated in AWR Design Forum 2018, RF/Microwave Design and Simulation Workshop
- Organiser & Supporting resource person: LabVIEW core1&core2 training.
- Getting started with Raspberry PI, Open CV and Machine Learning

- Organizer: One-week national level workshop on CST and Antenna Magus
- Participated & organized in programme, â€˜A Hands-on Training on CST Studio Suite and Antenna Magus.
- IEEE Computational Intelligence Society Distinguished Lecturer Program Technical Talk By Risto Miikkulainen
- ORGANIZER: WORKSHOP ON "SMART Homes. Measurements. Grids using IoT". By ECE&EEE
- ORGANIZER: 3 Day basic level training on LabVIEW & NI Hardware.
- RESOURCE PERSON: 3 Day basic level training on LabVIEW & NI Hardware. Batch 2
- ORGANIZER: 3 Day basic level training on LabVIEW & NI Hardware. Batch 2
- FDP: RF ANTENNA DESIGN AND EVALUATION, organized by E & ICT ACADEMY, NITW.
- Organizer: Workshop on Internet of Things.
- Organizer: Embedded sys workshop using TivaC, Orbit Booster Pack & Energia
- IOT Hands on Workshop using GISMO
- Industrial Visit: FACULTY MISSION TO SCHNEIDER ELECTRIC and RESOLUTE ELECTRONICS
- Lab View and NI USRP Part-2
- Lab View and NI USRP Part-1
- Use of ICT in Education for Online and Blended Learning.
- TEQIP Sponsored two-day National Conference on "Materials for specific Applications"
- Three-day FDP on "VLSI Digital Signal Processing"
- ISTE Short term training Programme on "Engineering Physics", by IITB
- Organizer: FDP on "Estimation Theory for Signal Processing and Cognitive Radio Networks"
- TEQIP Sponsored one day FDP on "Light Harvesting Materials".
- Organizer: MAD FUN-Mobile App Development
- Awareness workshop on "Outcome Based Education and Accreditation", by NBA
- A Three-Day Intensive Course on Electromagnetic Theory and Transmission Lines
- ISTE workshop on "Analog Electronics", by IITB
- ISTE workshop on "Akash for Education", by IITB
- "Enforcement of Intellectual Property Rights in Indian Scenario"
- Joint International Conference on "SEMCCO and FANCCO".
- 22nd Annual Conference of Indian Nuclear Society, INSAC - 2011
- Organizer: ICMID-2010
- AICTE sponsored SDP: "Comprehensive course on Embedded System Design"
- One day workshop on "Digital Signal Processing Applications in Medicine". in association with IEEE.

Memberships: IEEE student Member, IETE Student Member

External Examiner: (means lab external examiner, paper setting/moderator. Etc)
External Examiner: Microprocessor and Microcontroller Lab, EDC Lab, Microwave Lab

Paper Setting: EDC, EMTL, Microprocessor and Microcontroller, Optical Communications, Embedded Systems, Satellite Communications

AWARDS AND ACHIEVEMENTS:

Certifications:

- Certified as "LabVIEW Associate Developer" by National Instruments.
- Obtained Grade-B2 in Aptis-English Proficiency Test.
- Certified as "Oracle Certified Professional" in OCJP exam conducted by Oracle.
- Elite certificate in EM Theory, NPTEL
- Elite certificate in MWT, NPTEL
- Elite certificate in Antennas, NPTEL
- Successfully Completed Course on EMC, NPTEL

Appreciations: NO