Name and Designation: Dr. K. Jamal, Professor

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Educational Details:

➢ Ph.D (VLSI) from GITAM (Deemed to be University) in the year May 2021
➢ M.Tech in VLSI Design from Bharath Institute of Higher Education and Research(Deemed to be University), TN, in May 2005.
➢ ACTP (Advanced Certificate Trainee Program) Diploma in VLSI System Design from MSRSAS, Bangalore, 2015.
➢ B.Tech in Electronics and Communication Engineering from, JNTUH,AP in April 2003.

Professional Background:

➢ Working in Gokaraju Rangaraju Institute of Engineering and Technology (GRIET), Hyderabad from July 2008 to Till Date.

Administrative Experience:

• Serving as M.Tech.(VLSI) Class Coordinator from August 2021 to till date.
• BOS member for M.Tech(VLSI) of GRIET for the academic year 2019-20 onwards.
• Coordinator for conducting Technical Training to Government of Telangana Police Department for Sub-Inspector (Sis) and Police Constables (PCs)- SCT PCs and SIs (IT&C) from July-2020 to till date.
• Serving as Additional Controller of Examination (Valuation) from Nov 2017 to till date.
• Serving as Minority Cell Committee coordinator form August 2018 to till date.
• Served as PG (M.Tech) Coordinator from July 2011 to August 2017.
• Served as M.Tech.(VLSI, Embedded Systems) Class Coordinator from July 2008 to August 2017.
• Served as TEQIP Committee member under PG Education from July 2012 to August 2015.
• Served as QEEE System Admin from 2013 to Jan. 2016.

Committee Work:

➢ Minority Cell Committee coordinator form August 2018 to till date.

Paper Published:

International Journals:

1. Reddy, B. S. K., Mannem, K., & Jamal, K. (2021). Software Defined Radio Based Non-orthogonal Multiple Access (NOMA) Systems. *Wireless Personal Communications*, 1-23. **(SCIE)**
2. Jamal, K., Chari, K. M., & Srihari, P. (2019). Test pattern generation using thermometer code counter in TPC technique for BIST implementation. *Microprocessors and Microsystems*, *71*, 102890.**(SCI)**
3. Jamal, K., Srihari, P., Chari, K. M., & Sabitha, B. (2018). Low power test pattern generation using test-per-scan technique for BIST implementation. *ARPN Journal of Engineering and Applied Sciences*, *13*(8
4. Jamal, K., Srihari, P., & Kanakasri, G. (2016). Test Vector Generation using Genetic Algorithm for Fault Tolerant Systems. *International Journal of Control Theory and Applications (IJCTA)*, *9*(12), 5591-5598.
5. Jamal, K., & Srihari, P. (2016). Low power TPC using BSLFSR. *International Journal of Engineering and Technology (IJET)*, *8*(2), 759-e.

International Conferences:

1. Kumar, U. A., Chintakunta, R. K., Kumar, S., Jamal, K., & Ahmed, S. E. (2021, December). Approximate Multiplier Architectures for Error Resilient Applications. In *2021 IEEE International Symposium on Smart Electronic Systems (iSES)(Formerly iNiS)* (pp. 89-92). IEEE.
2. Yadav, S., Motupalli, R., Jamal, K., & Suresh, C. (2021, October). An Automated Rescue and Service System with Route Deviation using IoT and Blockchain Technologies. In *2021 IEEE Mysore Sub Section International Conference (MysuruCon)* (pp. 582-586). IEEE.
3. Reddy, Y. D., Reddy, T. S., & Jamal, K. (2021, November). An Efficient Interconnection System for Neural NOC Using Fault Tolerant Routing Method. In *Journal of Physics: Conference Series* (Vol. 2089, No. 1, p. 012069). IOP Publishing.
4. Sridevi, N., Jamal, K., & Mannem, K. (2021, August). Implementation of Cyclic Redundancy Check in Data Recovery. In *2021 Second International Conference on Electronics and Sustainable Communication Systems (ICESC)* (pp. 17-24). IEEE.
5. Sridevi, N., Jamal, K., & Mannem, K. (2021, April). Implementation of Error Correction Techniques in Memory Applications. In *2021 5th International Conference on Computing Methodologies and Communication (ICCMC)* (pp. 586-595). IEEE.
6. Sravya, G., Kumar, M. O., Reddy, Y. S., Jamal, K., & Mannem, K. (2020, December). The Ideal Block Ciphers-Correlation of AES and PRESENT in Cryptography. In *2020 3rd International Conference on Intelligent Sustainable Systems (ICISS)* (pp. 1107-1113). IEEE.
7. Supritha, B., Mannem, K., Reddy, B. V., Jamal, K., & Kumar, M. O. (2020, October). High speed and efficient ALU using modified booth multiplier. In *2020 Fourth*

International Conference on I-SMAC (IoT in Social, Mobile, Analytics and Cloud)(I-SMAC) (pp. 1196-1200). IEEE.

8. Nandam, K. S., Jamal, K., Budati, A. K., Mannem, K., & Kumar, M. O. (2020, June). Design of multiplier with dual mode based approximate full adder. In *2020 5th International Conference on Communication and Electronics Systems (ICCES)* (pp. 40-46). IEEE.

9. Jamal, K., Mannem, K., Kumar, M. O., & Reddy, B. V. (2020, June). FPGA Implementation of SIC Pair Generator. In *2020 5th International Conference on Communication and Electronics Systems (ICCES)* (pp. 62-67). IEEE.

10. Nandam, K. S., Jamal, K., Budati, A. K., Mannem, K., & Kumar, M. O. (2020). Design and analysis of Dadda multiplier with Common Boolean Logic. *Materials Today: Proceedings*, *33*, 4833-4836.

11. Sravya, G., Kumar, M. O., Sheeba, G. M., Jamal, K., & Mannem, K. (2020). Hardware lightweight design of PRESENT block cipher. *Materials Today: Proceedings*, *33*, 4880-4886.

12. Bhavani, G., Jamal, K., & Reddy, B. V. (2019, July). Weighted Pseudorandom Based Deterministic Bist for Fault Testing and Self-Healing. In *2019 International Conference on Communication and Electronics Systems (ICCES)* (pp. 192-197). IEEE.

13. Jamal, K., & Srihari, P. (2015, January). Analysis of test sequence generators for built-in self-test implementation. In *2015 International Conference on Advanced Computing and Communication Systems* (pp. 1-4). IEEE.

Workshops Organized:

1. Co-Coordinator for STTP on AI-MIMO MILLIMETER(mm) WAVE AND MASSIVE MIMO APPLICATIONS FOR 5G WIRELESS NETWORK USING AI, 26 April to 1 May 2021 organized by Department of ECE,GRIET.

2. Co-Coordinator for STTP on MILLIMETER(mm) WAVE AND MASSIVE MIMO APPLICATIONS FOR 5G WIRELESS NETWORKS USING AI, 16 to 21 Nov 2020 organized by Department of ECE,GRIET.

3. Organised Workshop on "Case study based workshop on IoT and Machine Learning", Feb 1-3,2018 By ECE Dept, GRIET

4. Coordinated in conduction of STCC on VLSI System Design organized by CDAC Hyderabad as part of the project entitled Capacity Building in the Area of Electronic Product Design and Product Technology conducted at GRIET,Hyderabad during the period from 16-10-2017 to 16-11-2017(120 hrs)

1. Organised Workshop on "Smart Homes Measurements Grids using IoT" from 10 to 12 October, 2017 held at GRIET, Hyderabad.

2. Organised Workshop on Embedded Systems Using TivaC, Orbit Booster Pack &Energia, 15-16 Dec-2016.held at GRIET, Hyderabad.

3. Organised Workshop on Embedded Systems Using TivaC, Orbit Booster Pack &Energia, 6-7,Dec-2016.held at GRIET, Hyderabad.

4. Organized Hands on IOT workshop on "sensor to cloud" Oct 19-21, 2016 held at GRIET, Hyderabad.

5. Organized three dayFaculty Development Program on "*VLSI Digital Signal Processing*" 28th to 30th January, 2016,held atGRIET, Hyderabad.

6. Organized Workshop on *"EstimationTheory for Signal Processing and Cognitive Radio Networks"*, under TEQIP-II from October 15-17, 2015held at GRIET-Hyderabad.

7. Organised Workshop on Digital Design through Cadence Tools on 26-28 February,2013 under TEQIP-Phase II, held at GRIET,Hyderabad.

Workshops attended:

- Certified "Bits and Bytes of Computer Networking" an online non-credit course authorized by Google and offered through Coursera, March 2022.
- Certification on "Python Basics for Data Science" from Edx
- Full Custom Design of an IC Using Mentor Graphics and Synopsys during 09 – 21, August 2021, MVSR, Hyderabad.
- Faculty Development Program on Student Assessment and Evaluation 30 June to 3 July 2021,GRIET,Hyderabad.
- Online FDP on "Effective Online Teaching: Inline to be Online by Dr. Jandhyala N. Murthy, Director, GRIET, Hyderabad. Jun 25, 2021
- AICTE Sponsored Two Week Faculty Development Programme(FDP) on "Teaching-Pedagogical Intelligence" organized by Dept. of ECE from 15th March to 27th March 2021. MREC,Hyderabad.
- "Digital Design using Verilog HDL programming with practical" udemy online course on sep 25,20202.
- "Certificate for Completion of LaTeX Training" from 27-04-2020 to 02-05-2020 Spoken Tutorial Project, IIT Bombay, funded by National Mission on Education through ICT, MHRD, Govt., of India.
- 5 Day FDP on Arduino from 29.04.2020 to 03.05.2020 organized by Department of ECE, PSCMRCET in Associate with Spoken Tutorial, IIT Bombay- PMMMNMTT and securedr A+ grade.
- Two day workshop on "AI-IOT Conclave on Applications in Electronics and Communication Engineering" 28-29 Feb 2020, GITAM(Deemed to be University),Hyderabad.
- One day Workshop on "eSim, a First Course in the IoT Series for Teachers" held at GRIET, Kukatpally on 21 September 2019.
- NPTEL Online Certification on Digital Circuits-July-October-2018.(Elite).
- Attended Four Week FDP201X on Pedagogy for Online and Blended Teaching-Learning Process conducted by IITB from May 3, 2018 to May 30, 2018.
- Attended Four Week "Foundation Program in ICT for Education", IIT BombayX-FDP101X- 8th march to 12 April 2018
- Attended One Week Faculty Development Programme(FDP) on "Recent Advancements in Xilinx and ASIC Designs" (16-12-2017 to 21-12-2017) Organized by Department of ECE JNTUA College of Engineering Ananthapuramu (Autonomous) in association with National Institute of Technology, Warangal.
- Attended One Week Workshop on "VLSI design and Sensors in Systems", organised by School of Physics, University of Hyderabad, Hyderabad During 20th – 25th March 2017.
- Faculty Development Program on "Digital Signal Processing - Fundamentals and applications" *30th November - 5thDecember, 2015Organized by* Department of Electronics & Communication Engineering, Geethanjali College of Engineering & Technology, Hyderabad.
- FDP on "Light Harvesting Materials" held at GRIET, Hyderabad, on 10th October 2015.

- Workshop on "*National Programme on Technology Enhanced Learning*" (IITM) on 19th September 2015. Held at GPREC-Kurnool.
- FDP on "*Guide to a Passionate Teacher*", from 29-06-2015 to 04-07-2015 held at GRIET-Hyderabad.
- Workshop on "*Introduction to Design of Algorithms*", conducted by IIT Kharagpurfrom 25-05-2015 to 30-05-2015 held at GRIET,Hyderabad
- Skill development session in the "*Aero and Defense Industry Summit*", 14 Nov 2014. Hotel Novotel, Hyderabad.
- Workshop on "*Management Capacity Enhancement Programme*" Held at IIM-Indore during 27Oct to 02 Nov,2014
- Workshop on "*Quality Enhancement in Engineering Education*(*QEEE*)" organizes by IITM, Chennai (June 20,21-2014)
- "Free and Open Source Software Applications"organized by UGC Academic Staff College(JNTUH) 10-03-2014 To 12-03-2014 (3 DAYS)
- Two week ISTE workshop on Analog Electronics conducted by IIT-Kharagpur from 4 to 14 June,2013 held under the National Mission on Education through ICT(MHRD)
- FDP on "*Research Methodology*" organized by GITAM University, Hyderabad,28 Nov 2013 to 30 Nov 2013.
- FDP on "*Research Methodology*" organized by GITAM University, Hyderabad, 03 Oct 2013 to 5 Oct 2013.
- Two day ISTE workshop on "Aakash for Education" conducted by IITB on 10-11, Nov-2012 held at GRIET,Hyderabad.
- Workshop on "VLSI Design Methodologies" on 6-7 March 2009 held at BVRIT,Hyderabad.
- Workshop on "Low Power VLSI Design Methodologies" on 11-12 December 2009, held at Synopsys(india),Hyderabad.

Memberships:

- Member of **IETE** (The Institute of Electronics and Telecommunication Engineers).

Awards:

- Awarded International best researcher in VLSI Testing-26 Jan 2020 from RULA Awards

Appreciations:

- Certification of Appreciation from CDAC Hyderabad for active coordinated in conduction of STCC on VLSI System Design organized by CDAC Hyderabad, Nov 2017.